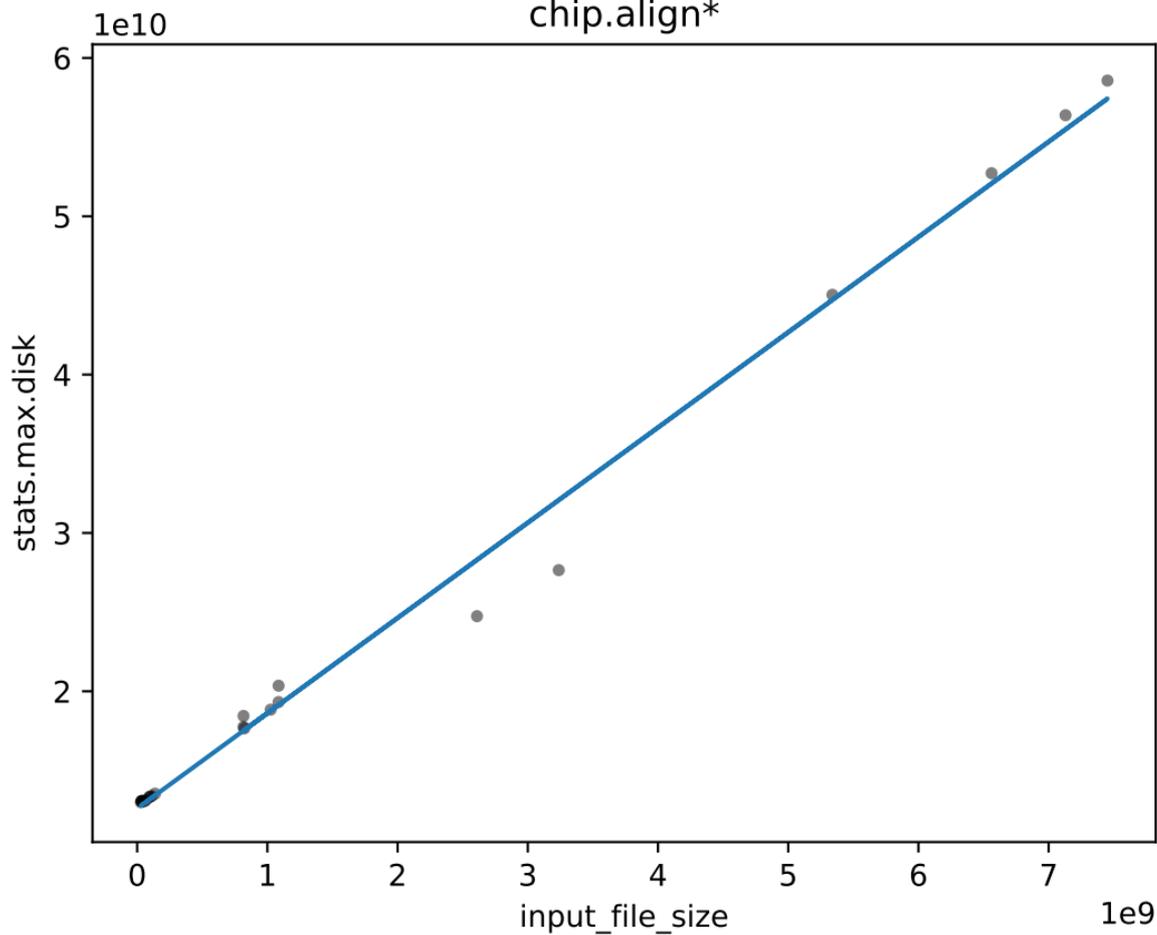
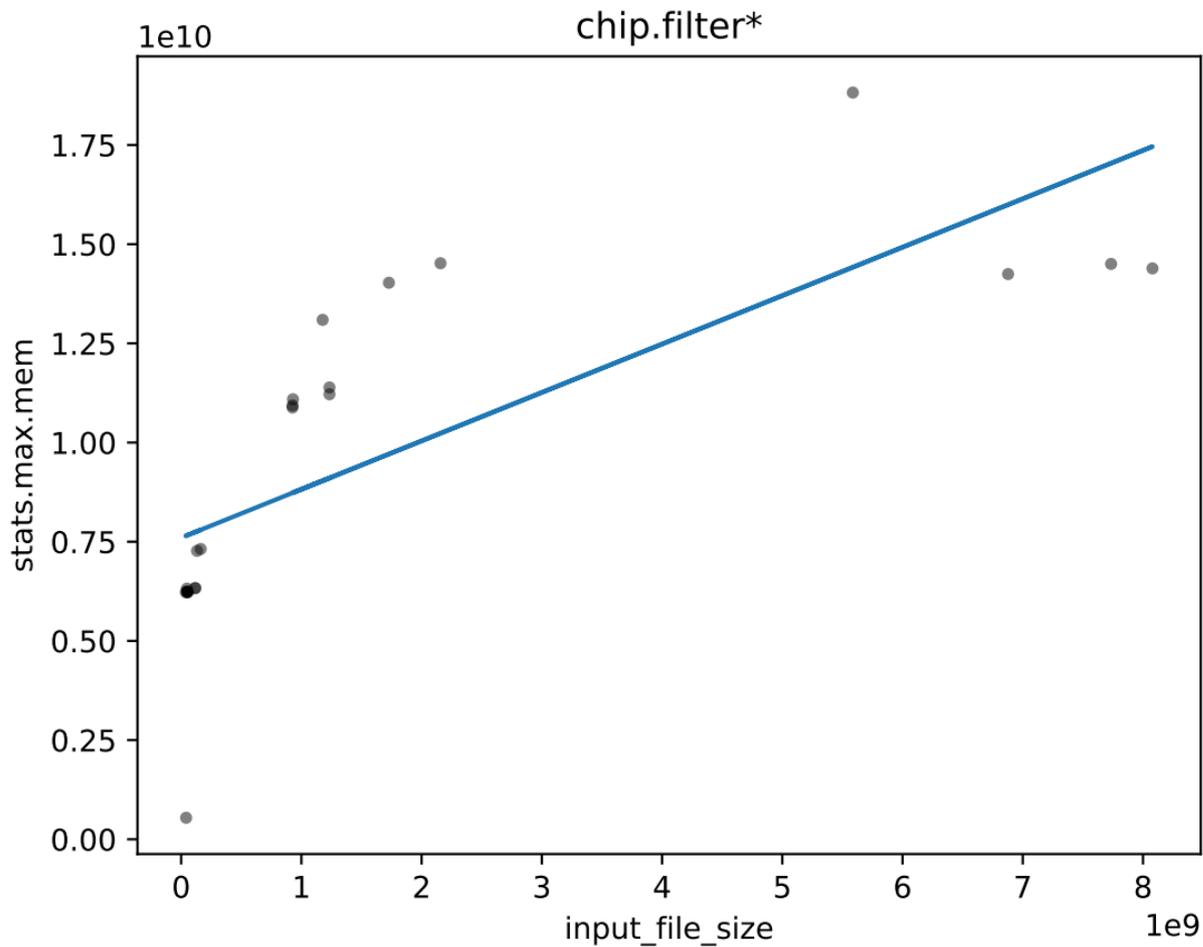


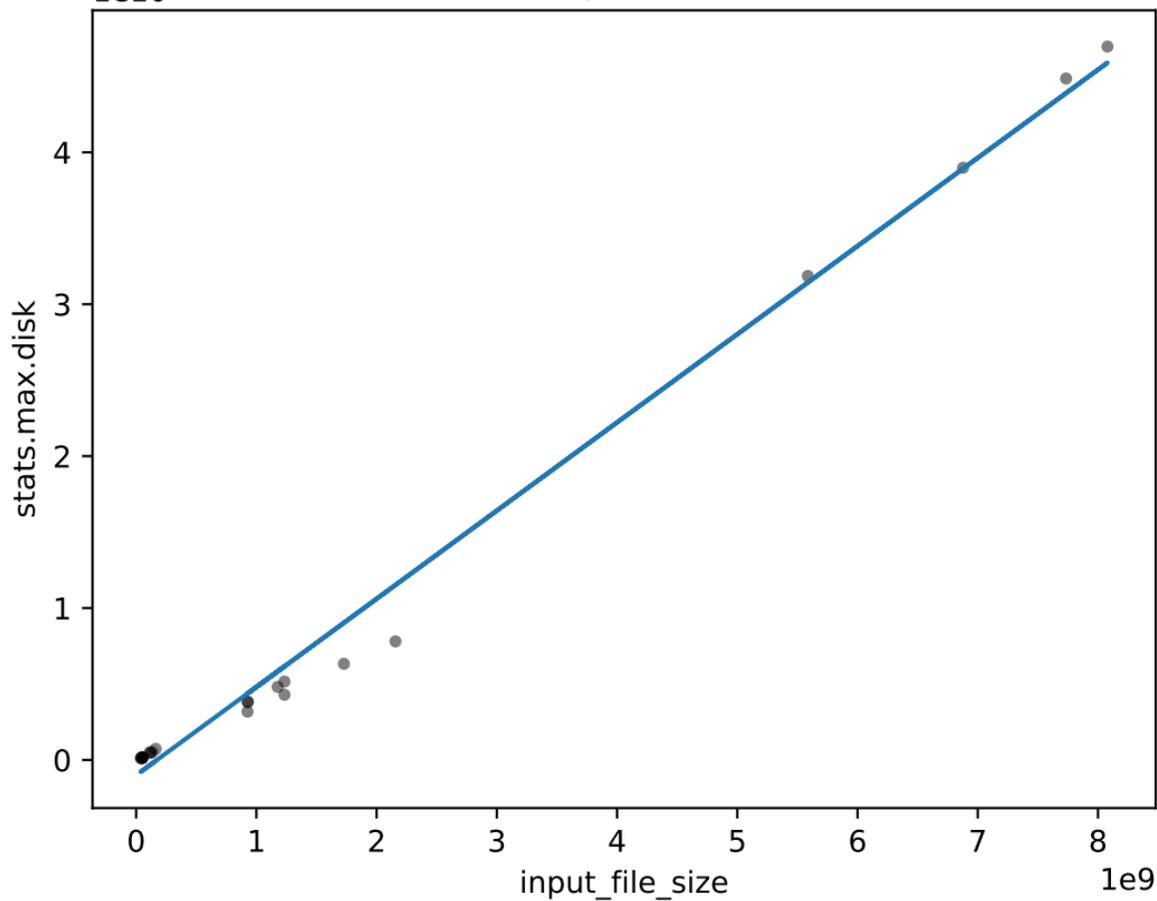
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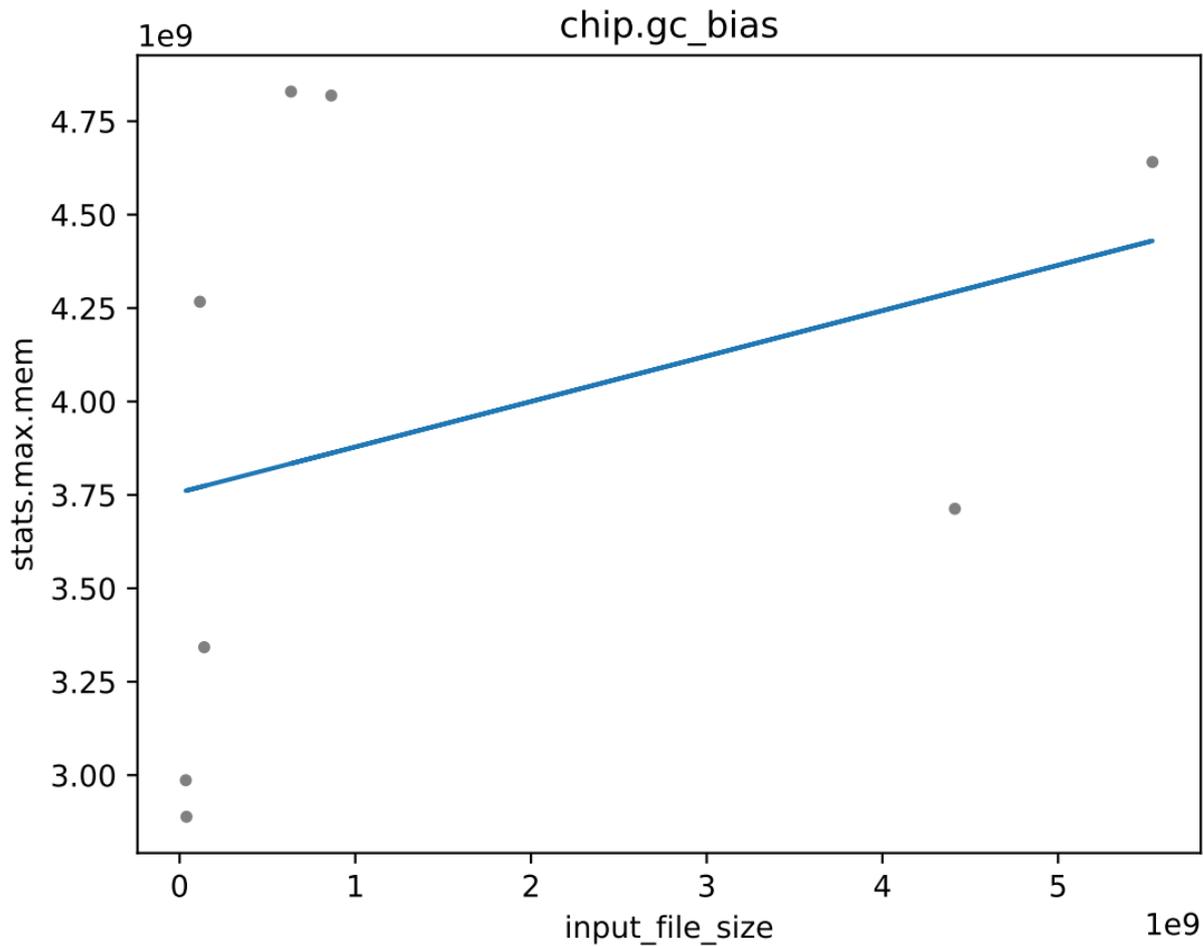




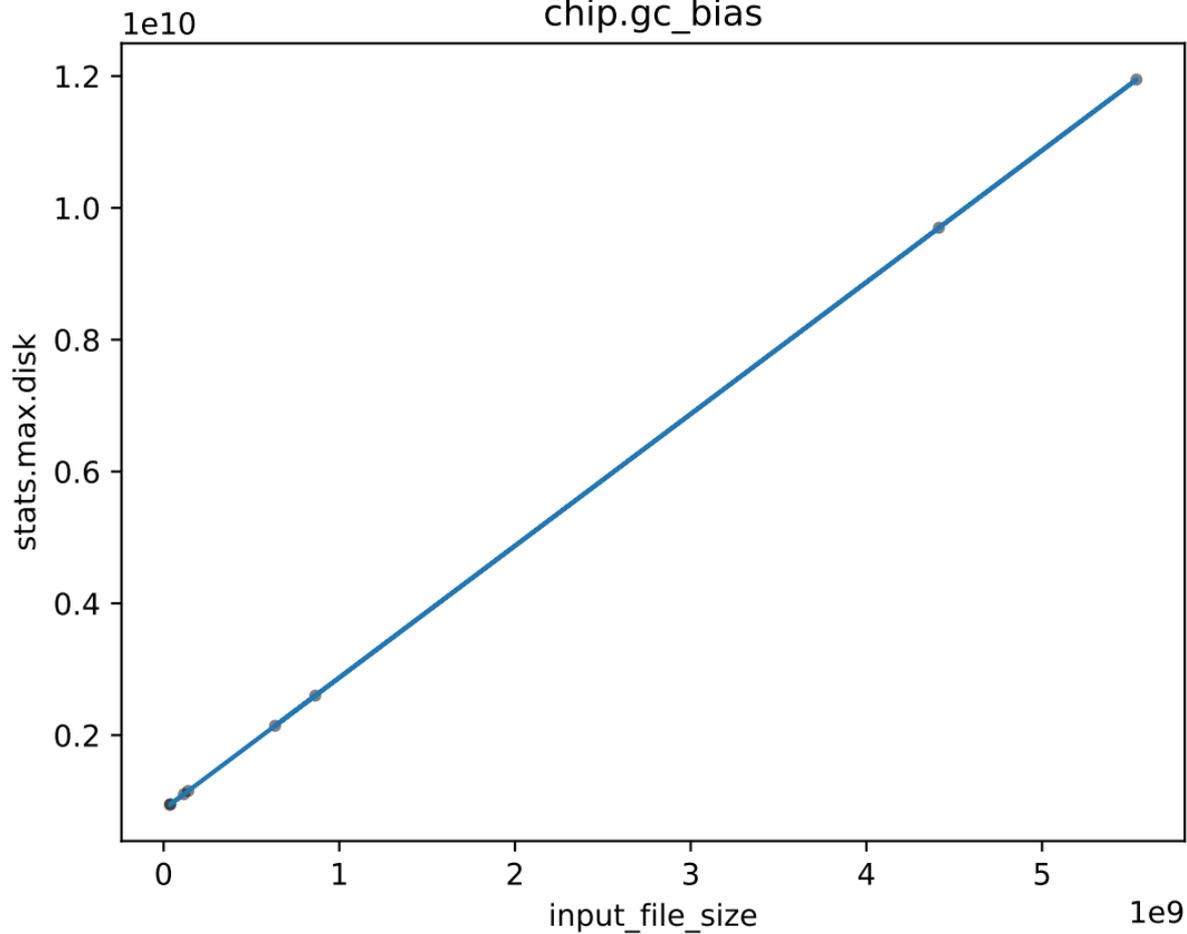
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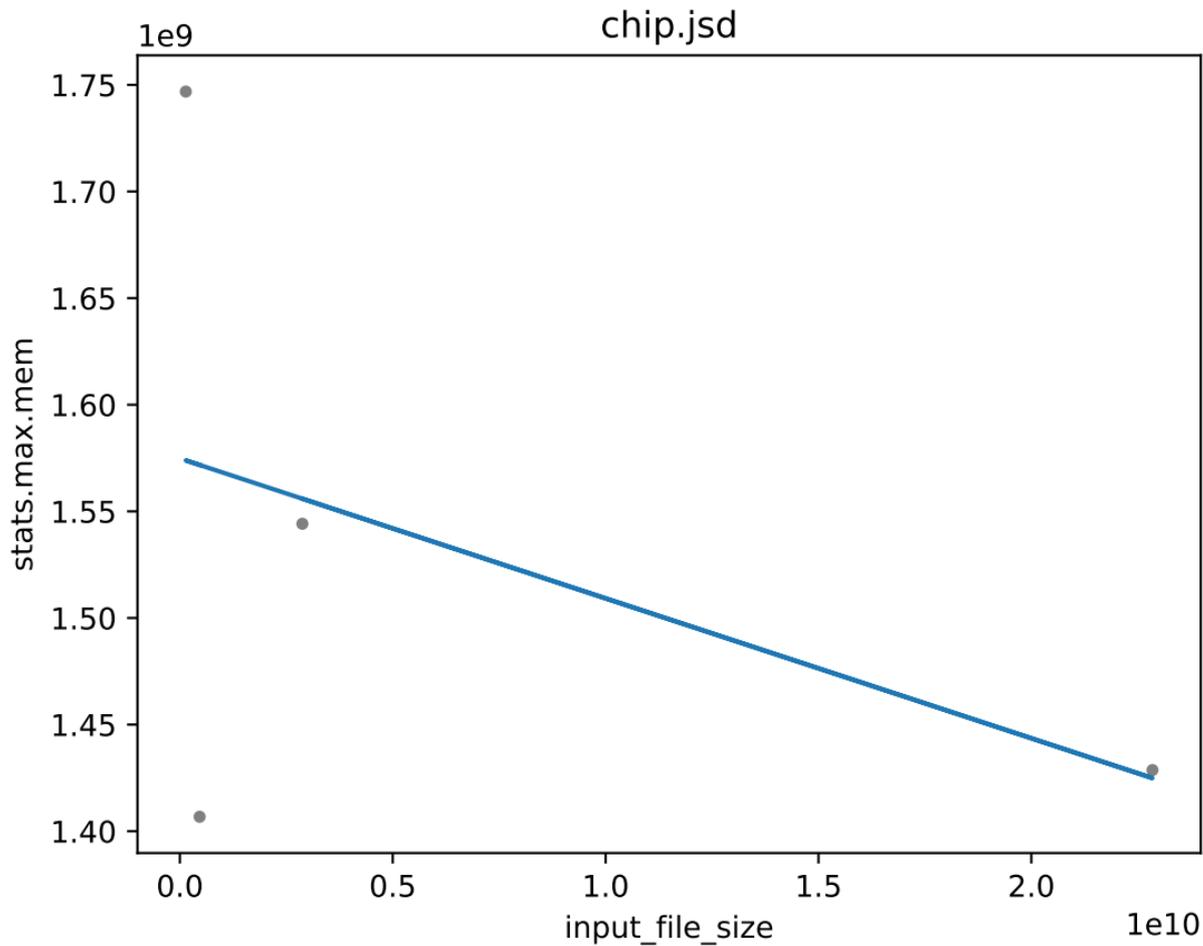
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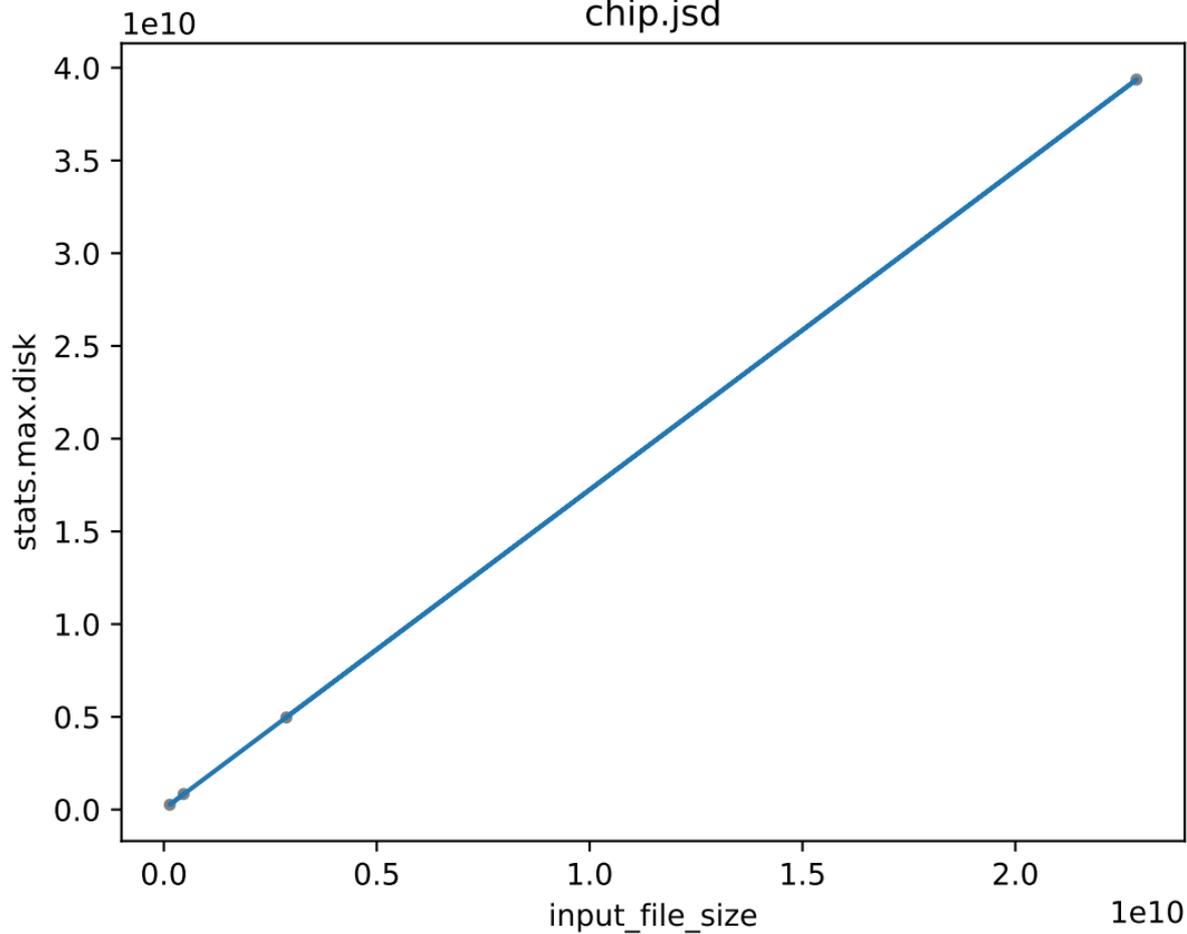


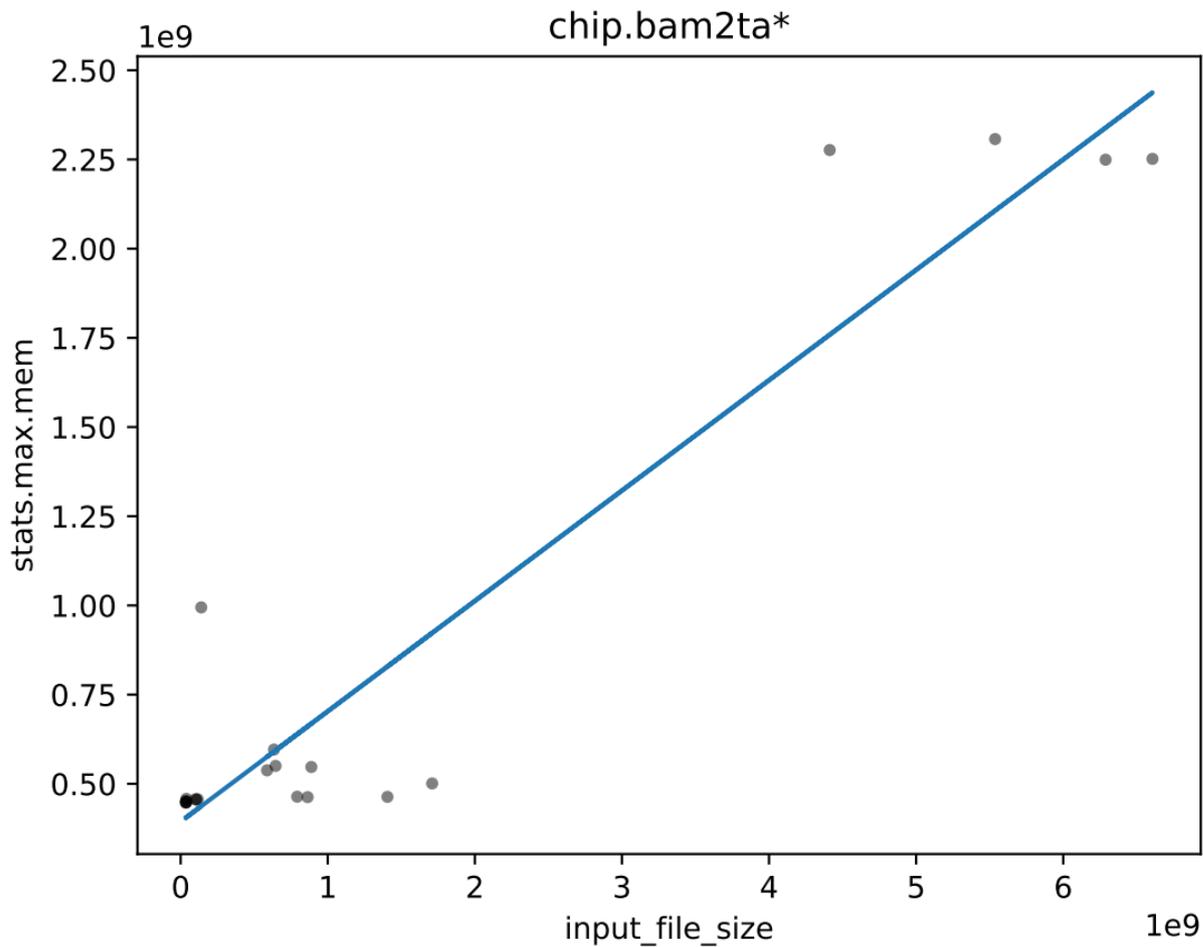
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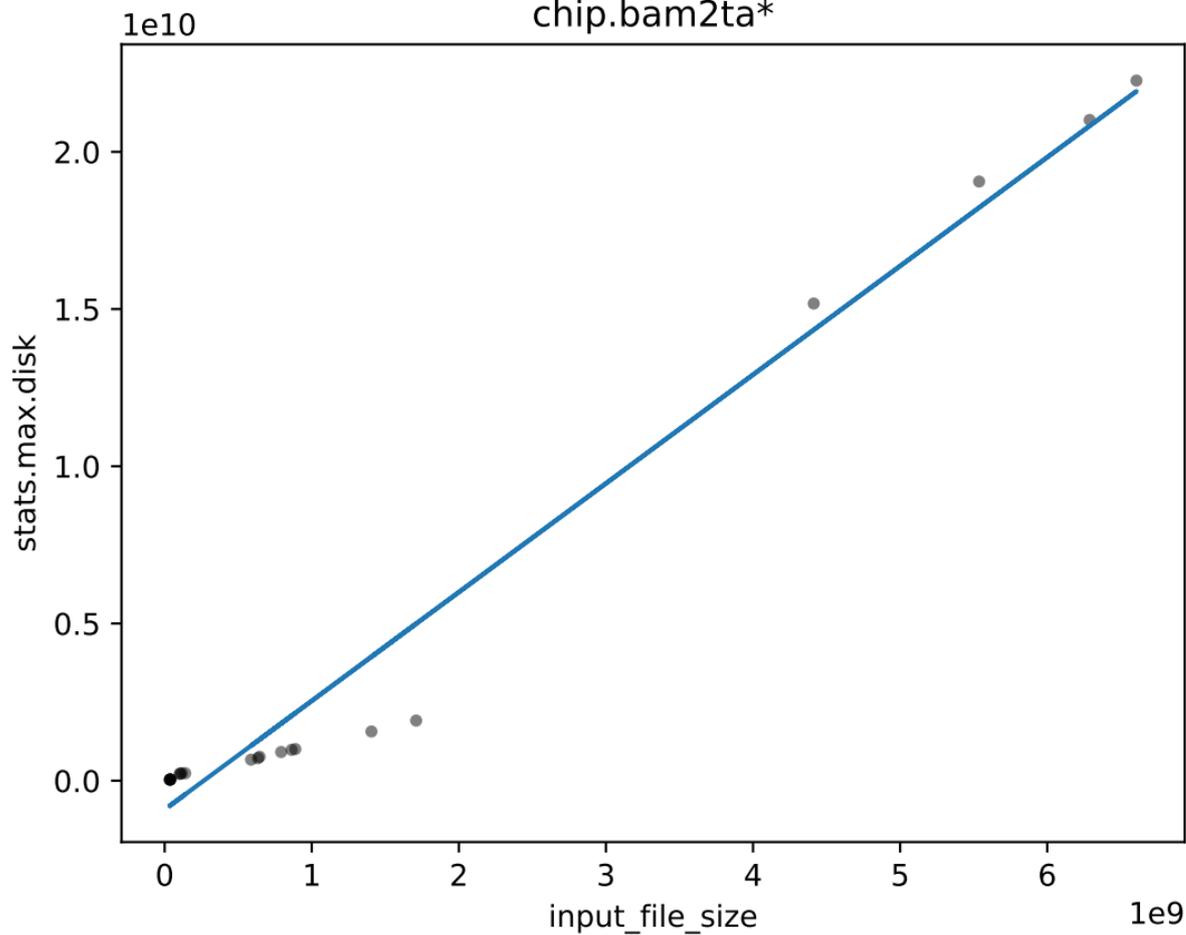


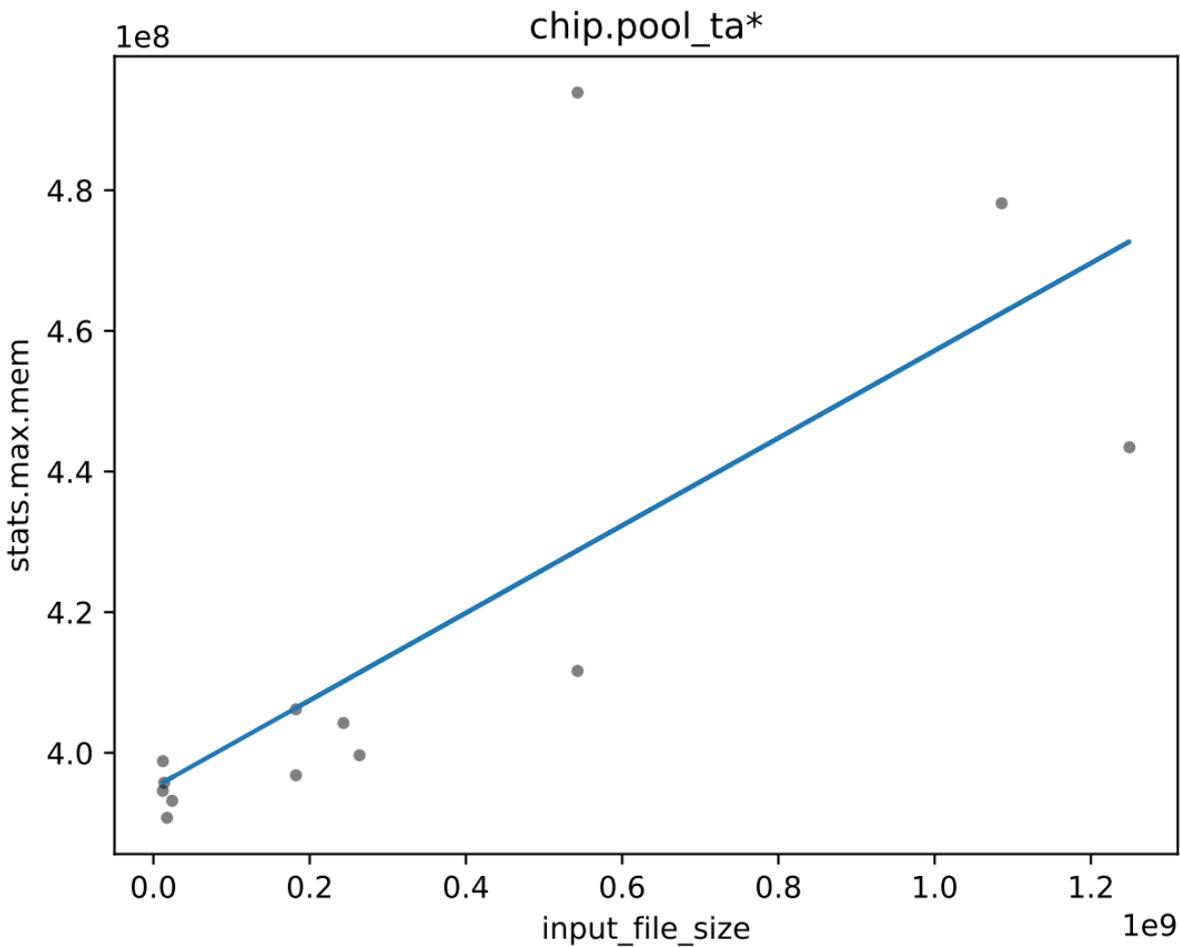
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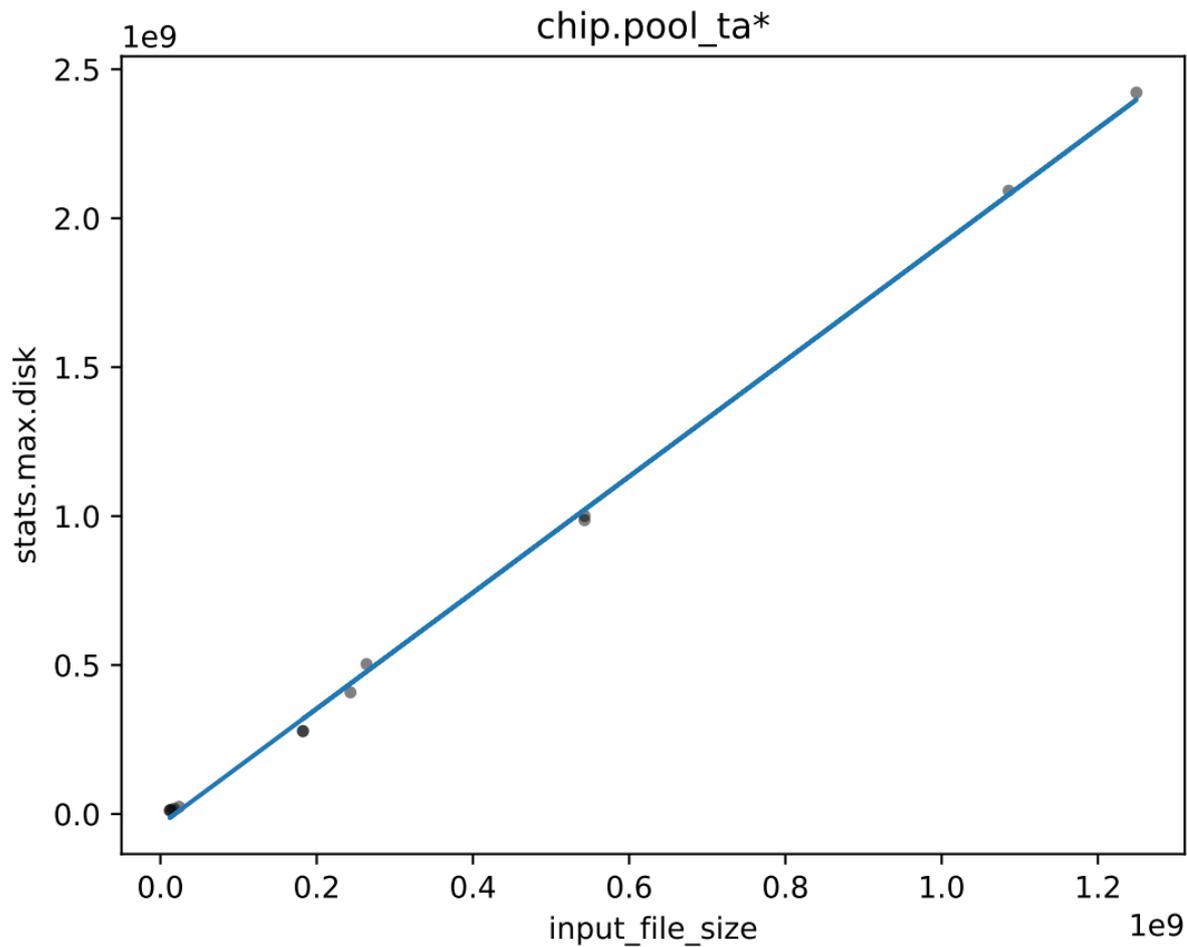




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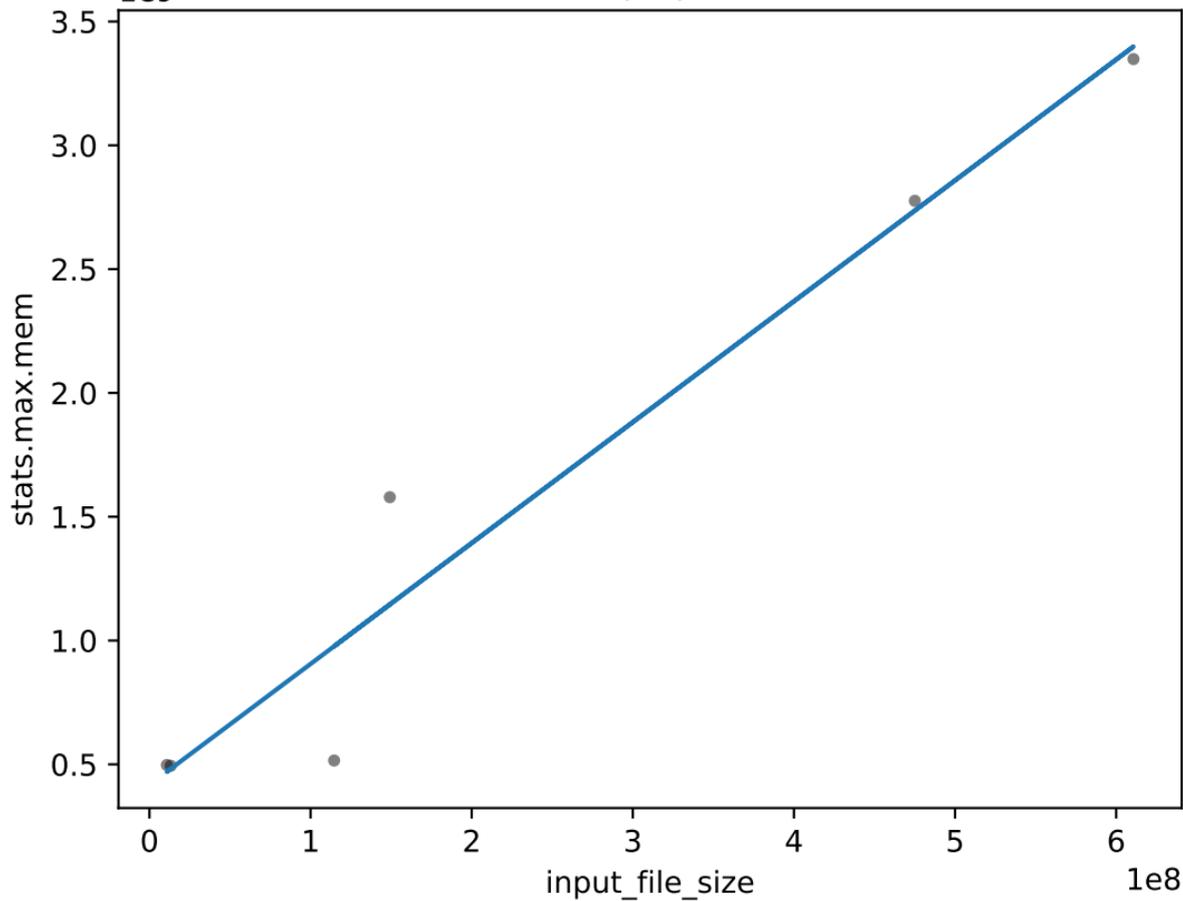




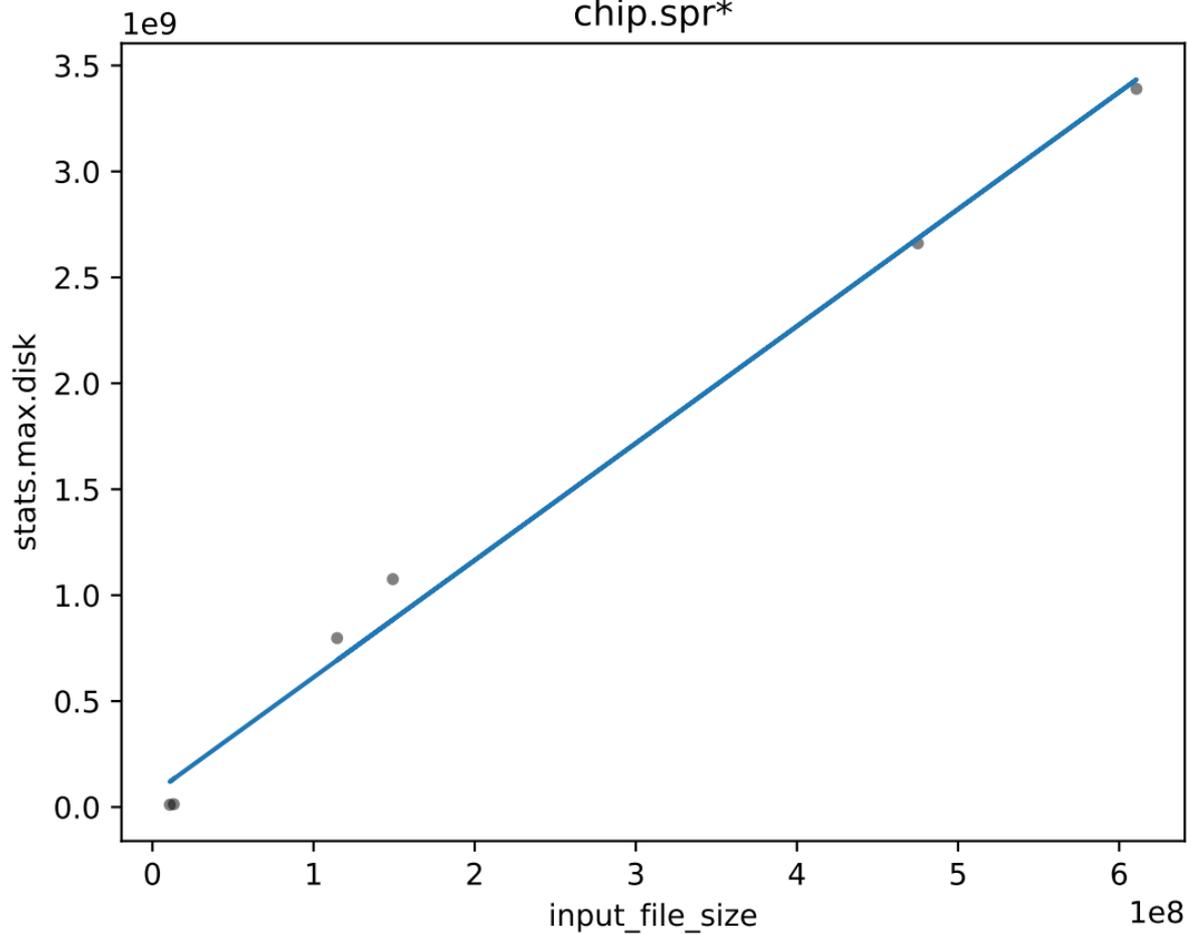


chip.spr*

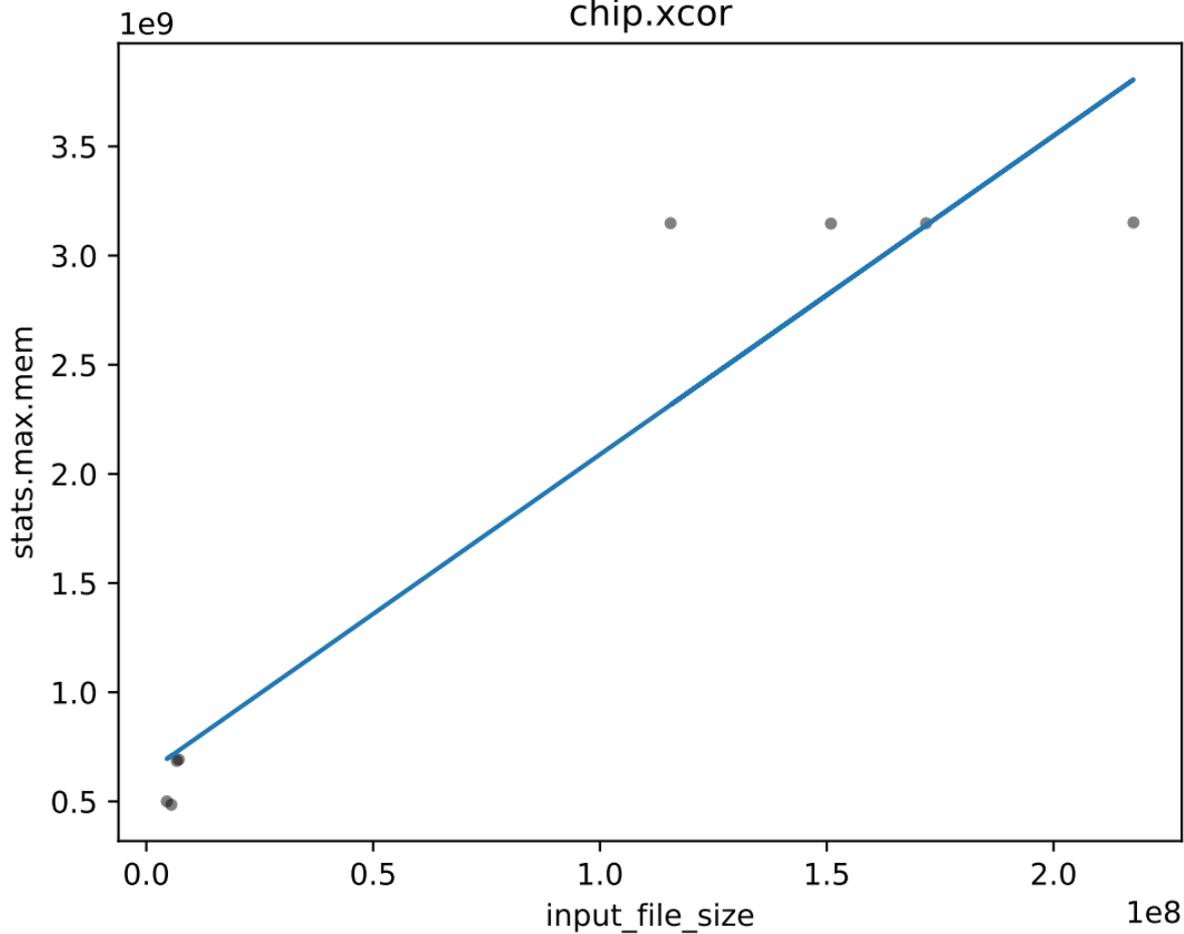
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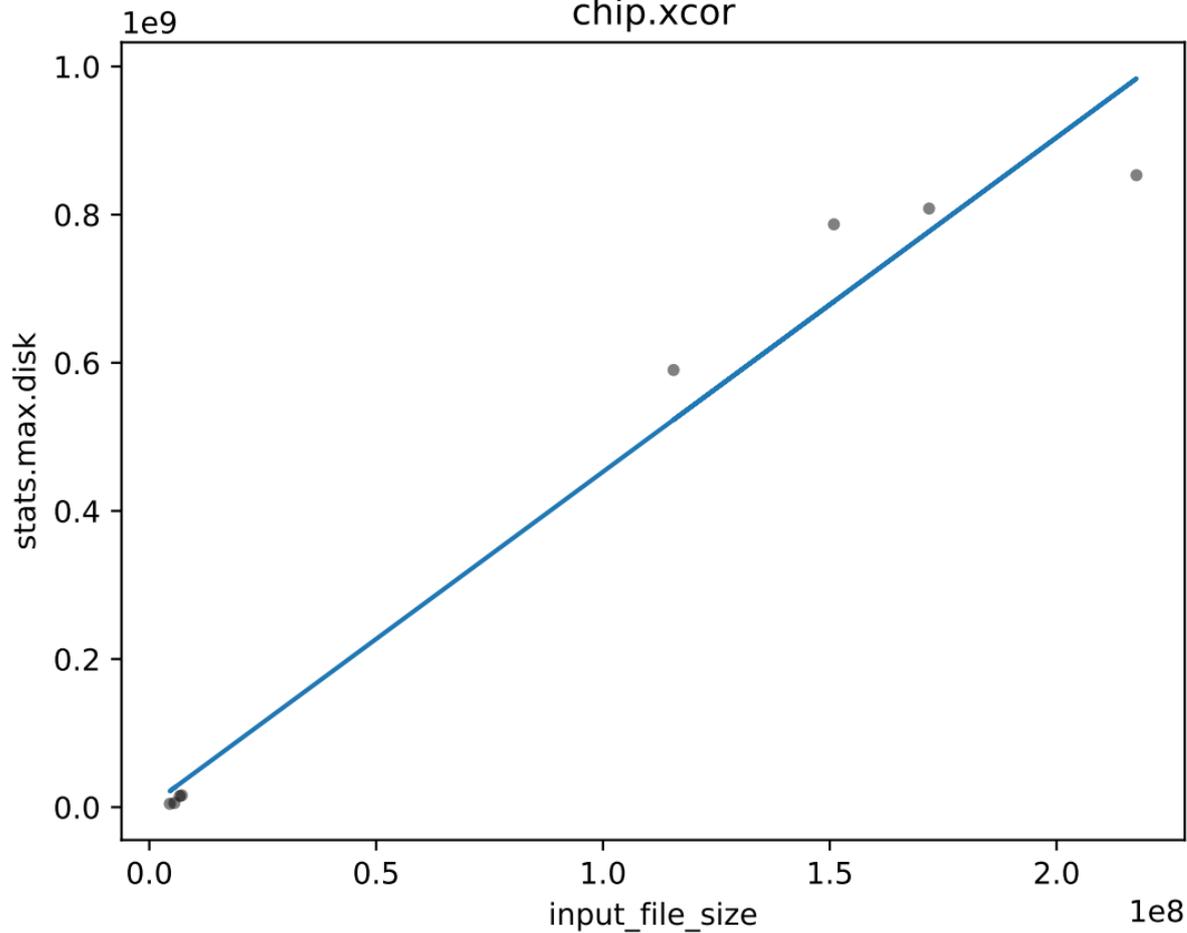
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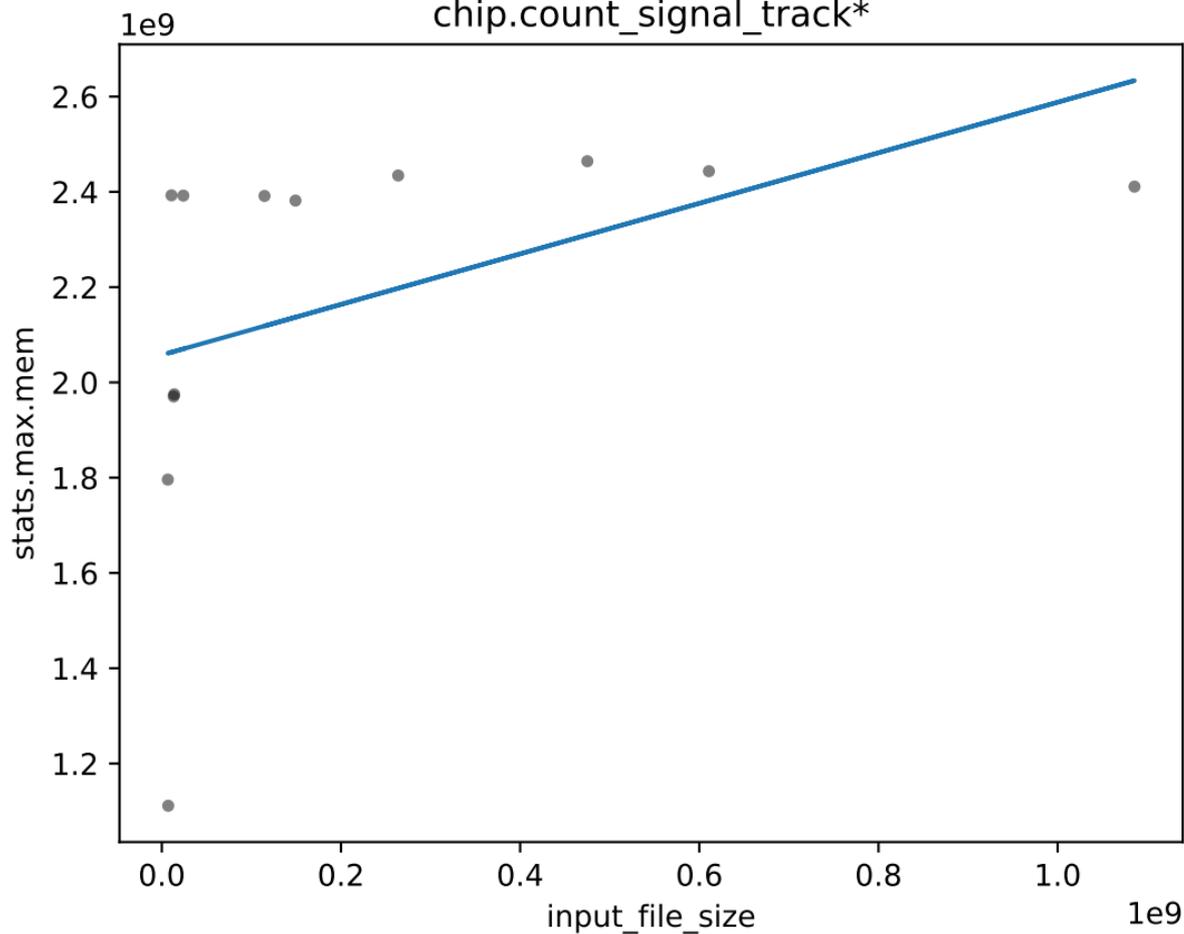
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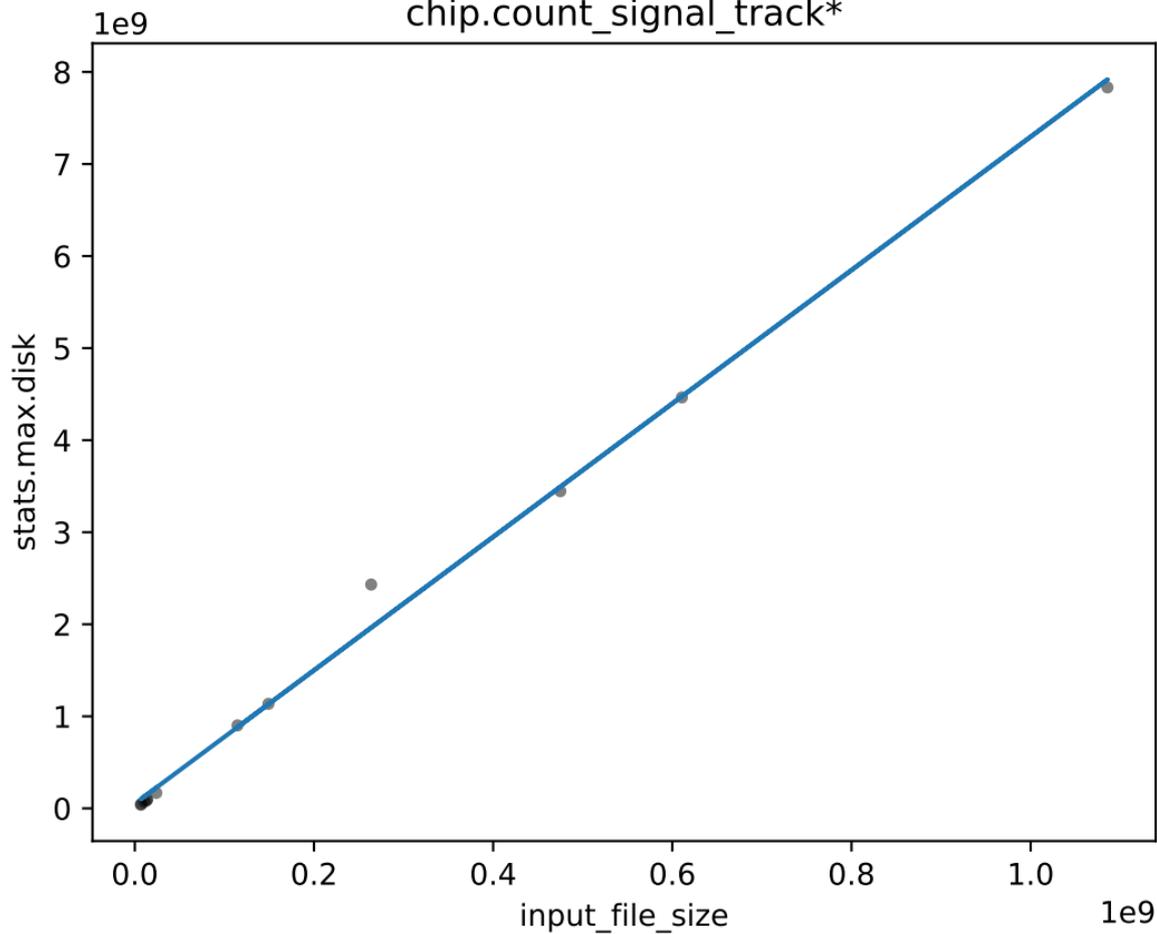
chip.xcor

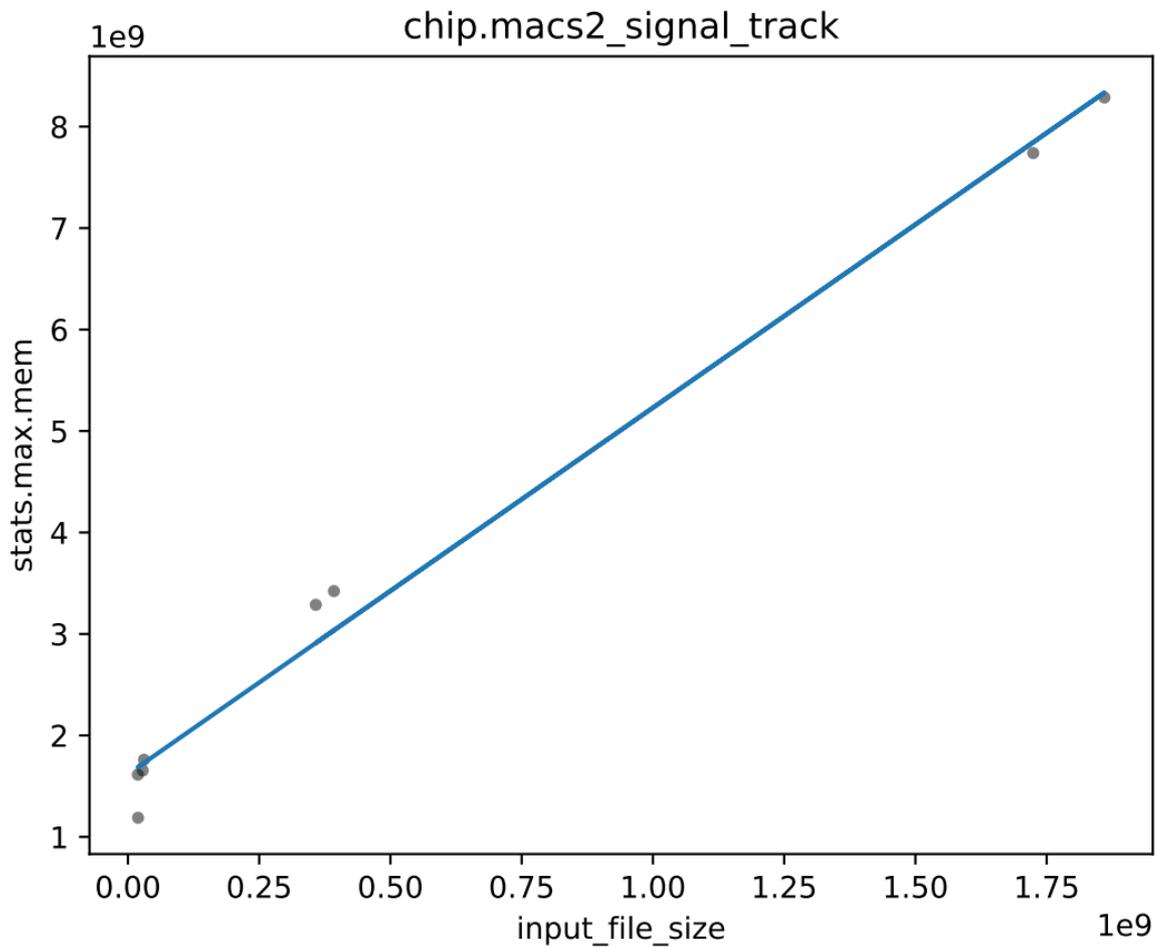


chip.count_signal_track*



chip.count_signal_track*





chip.macs2_signal_track

