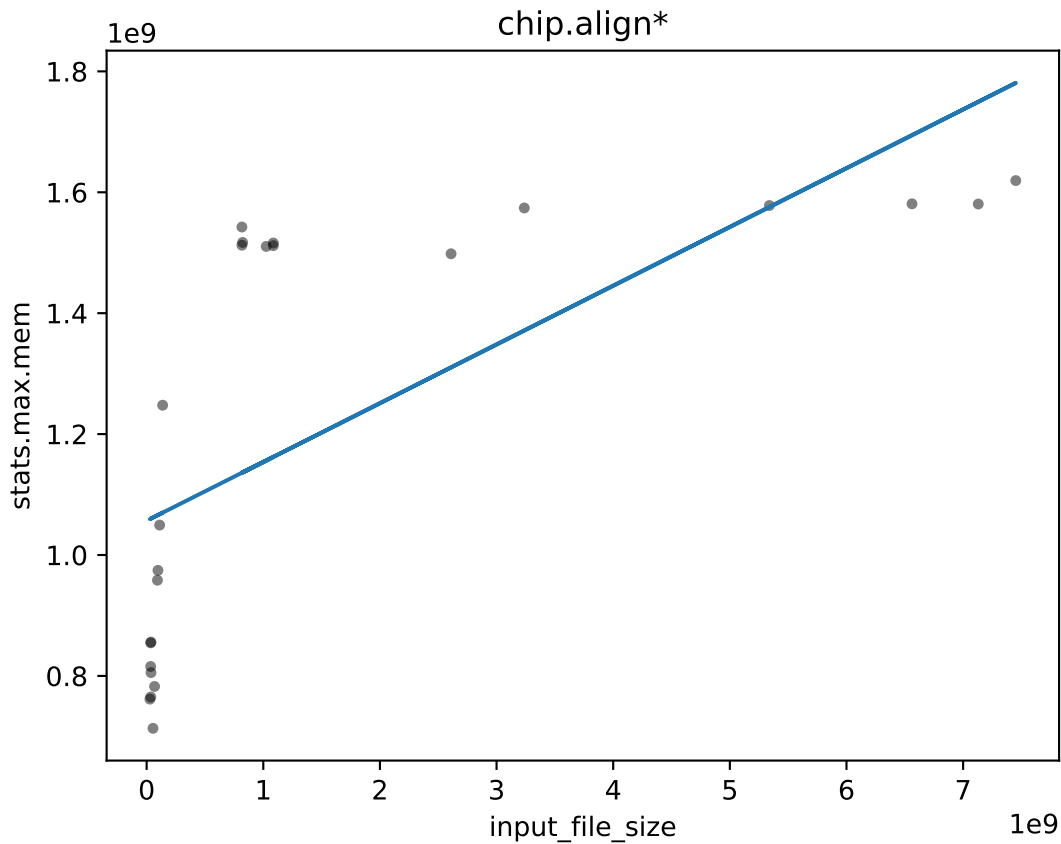
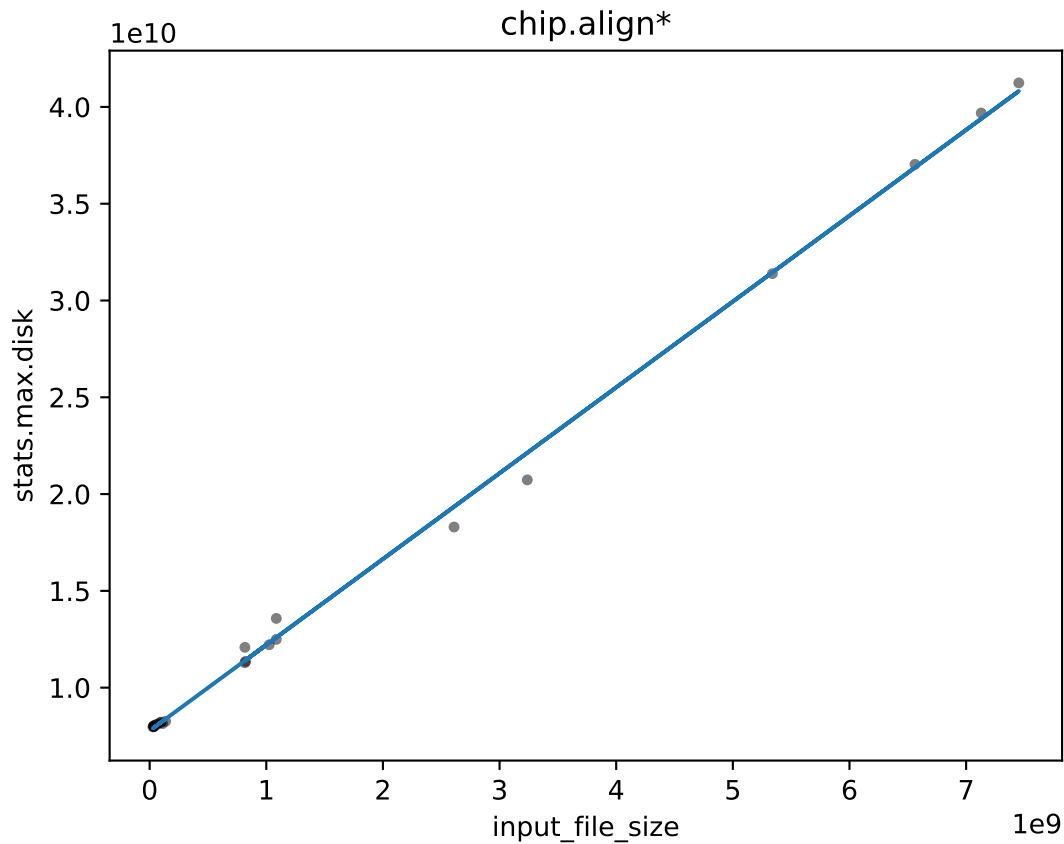
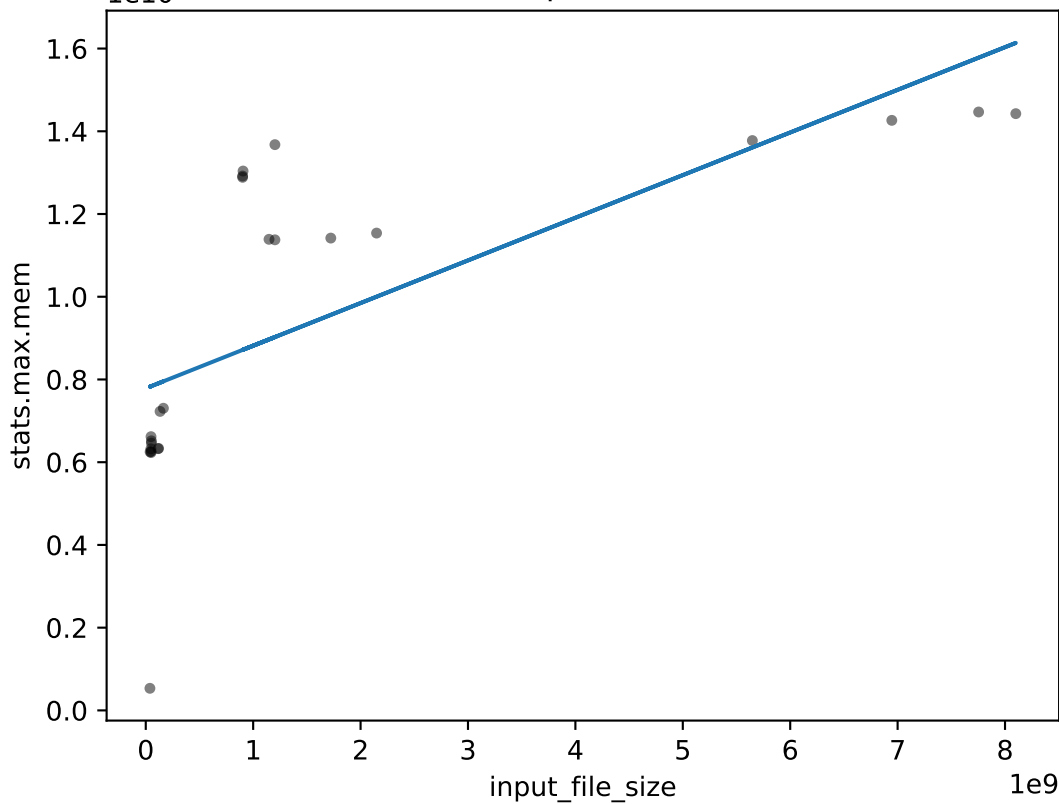






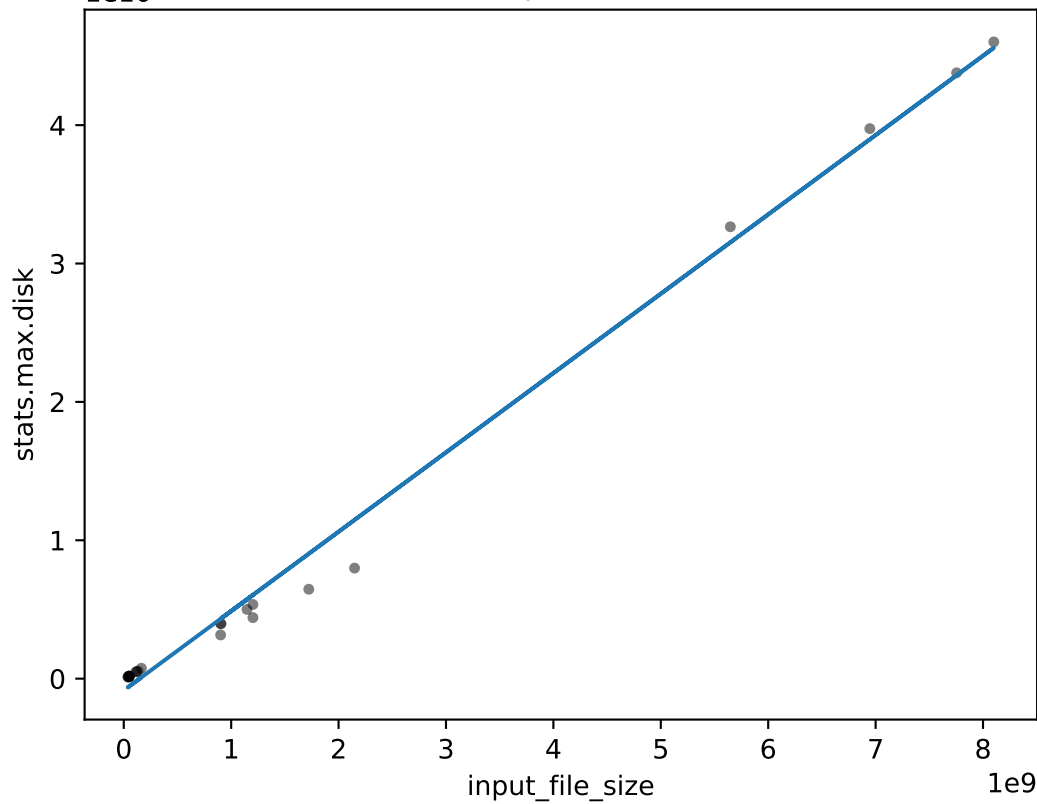
chip.filter*

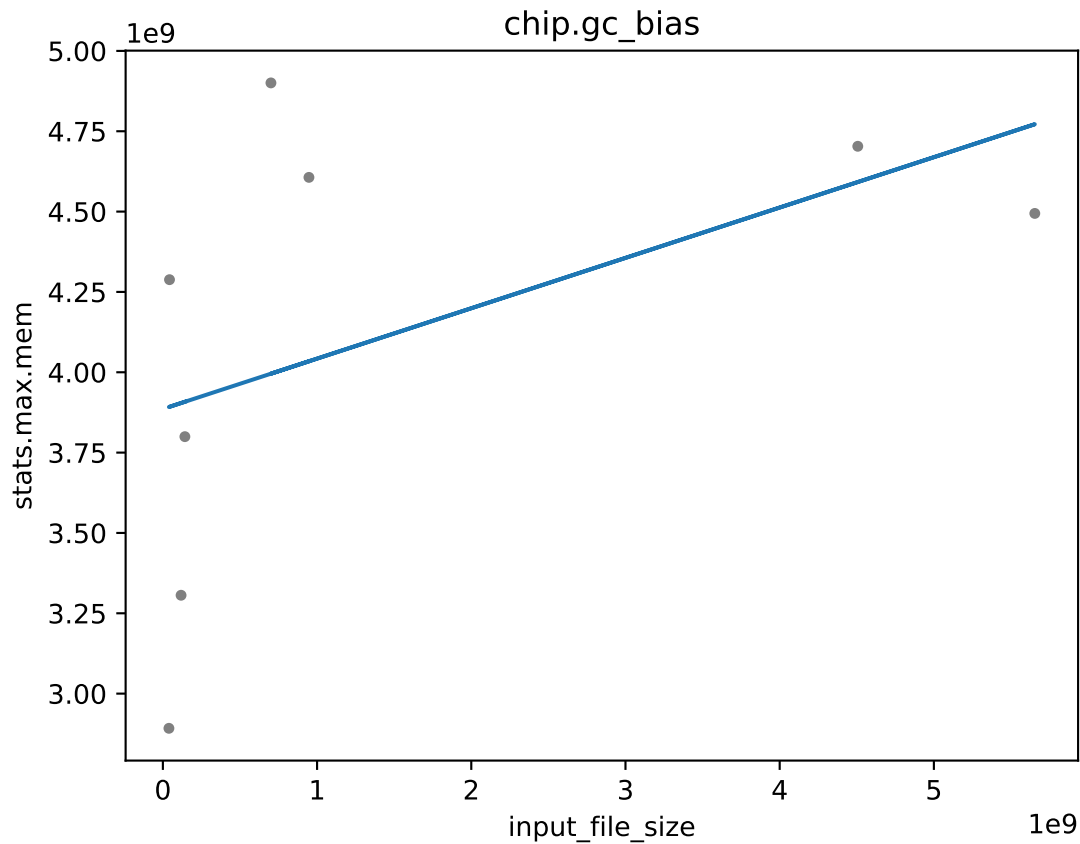
1e10

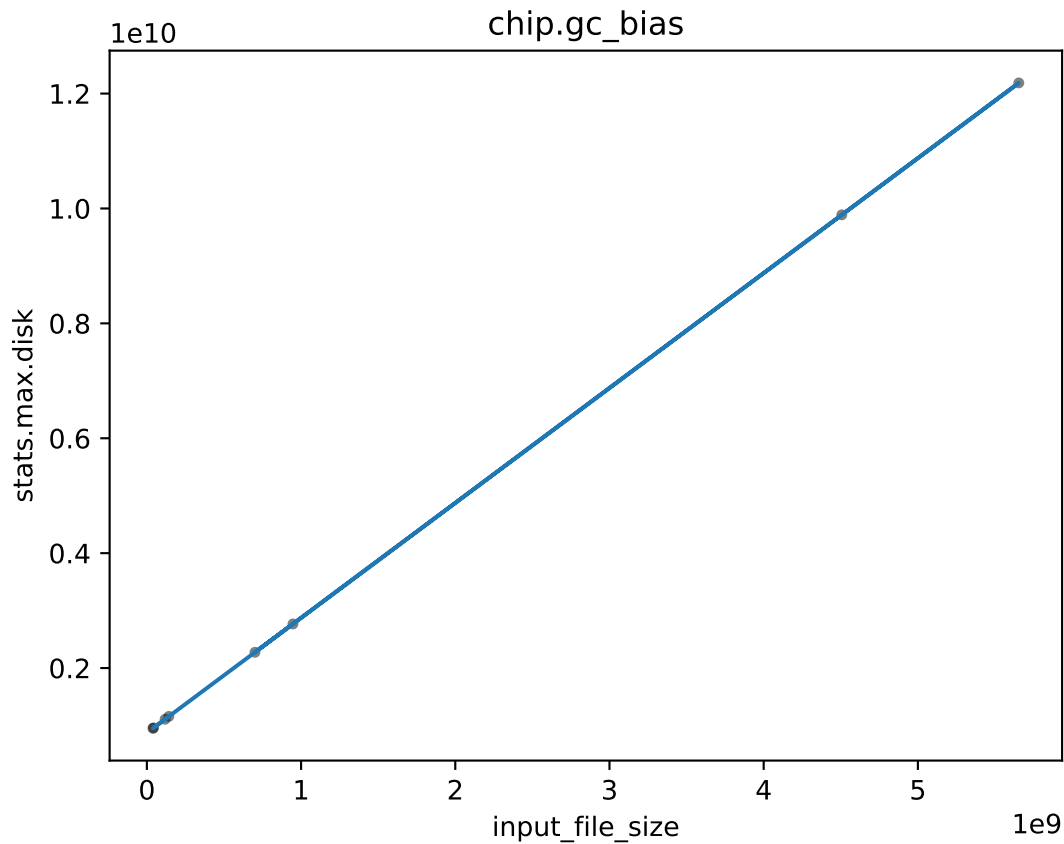


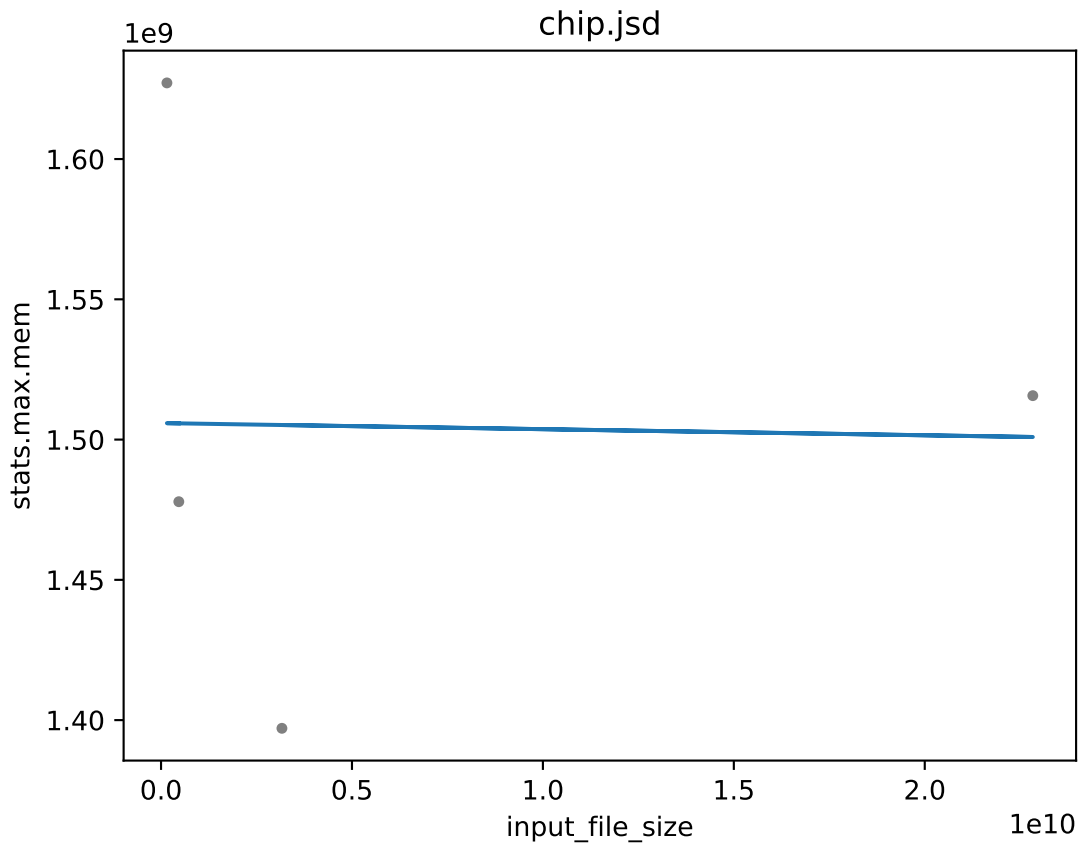
chip.filter*

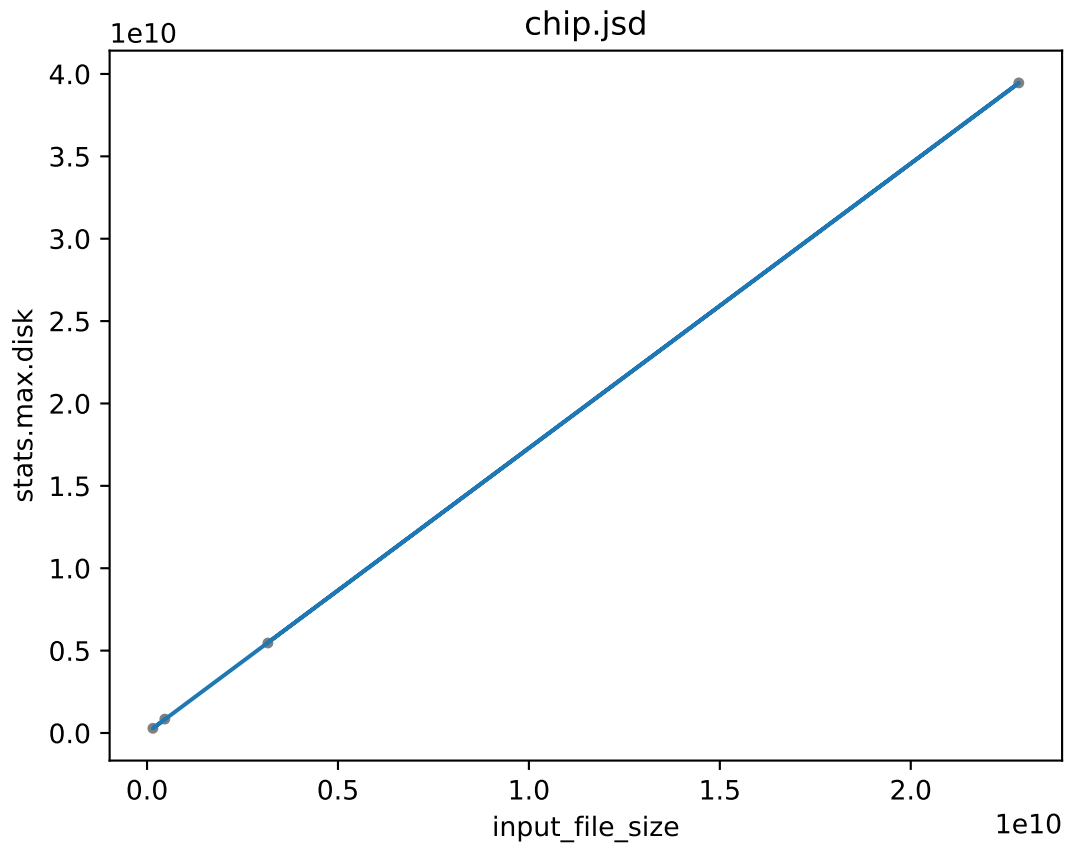
1e10

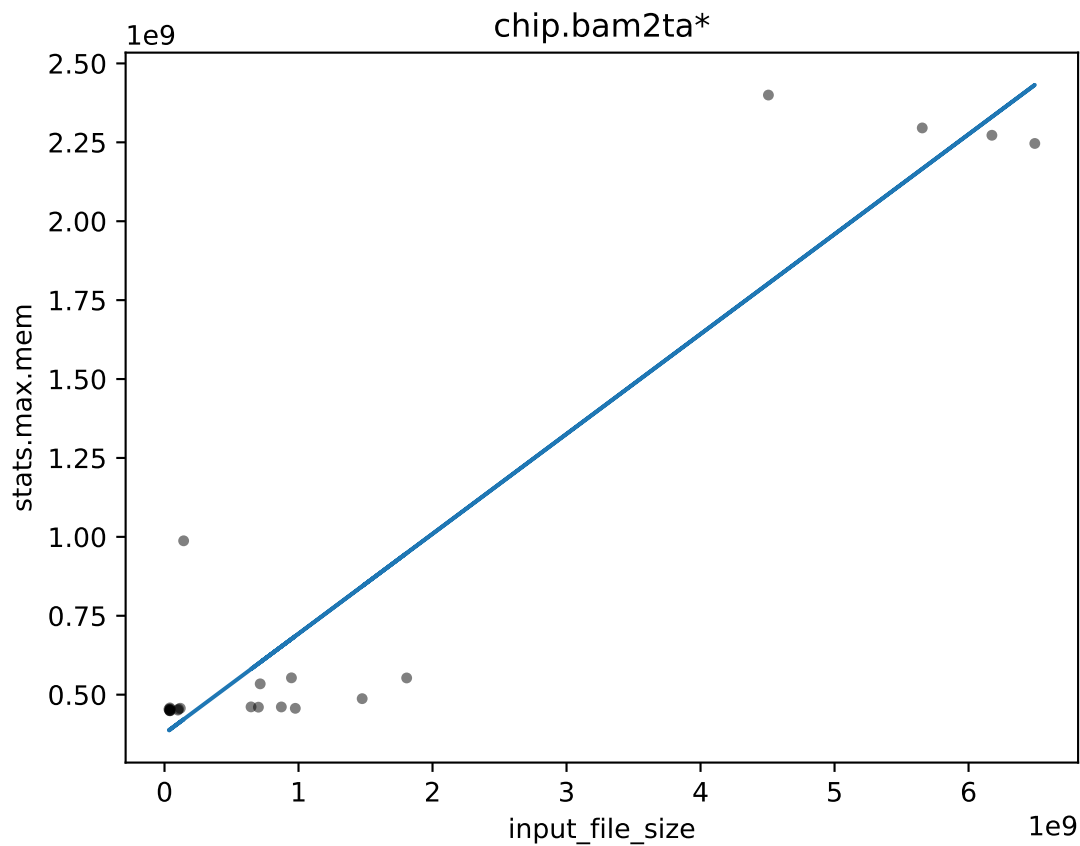


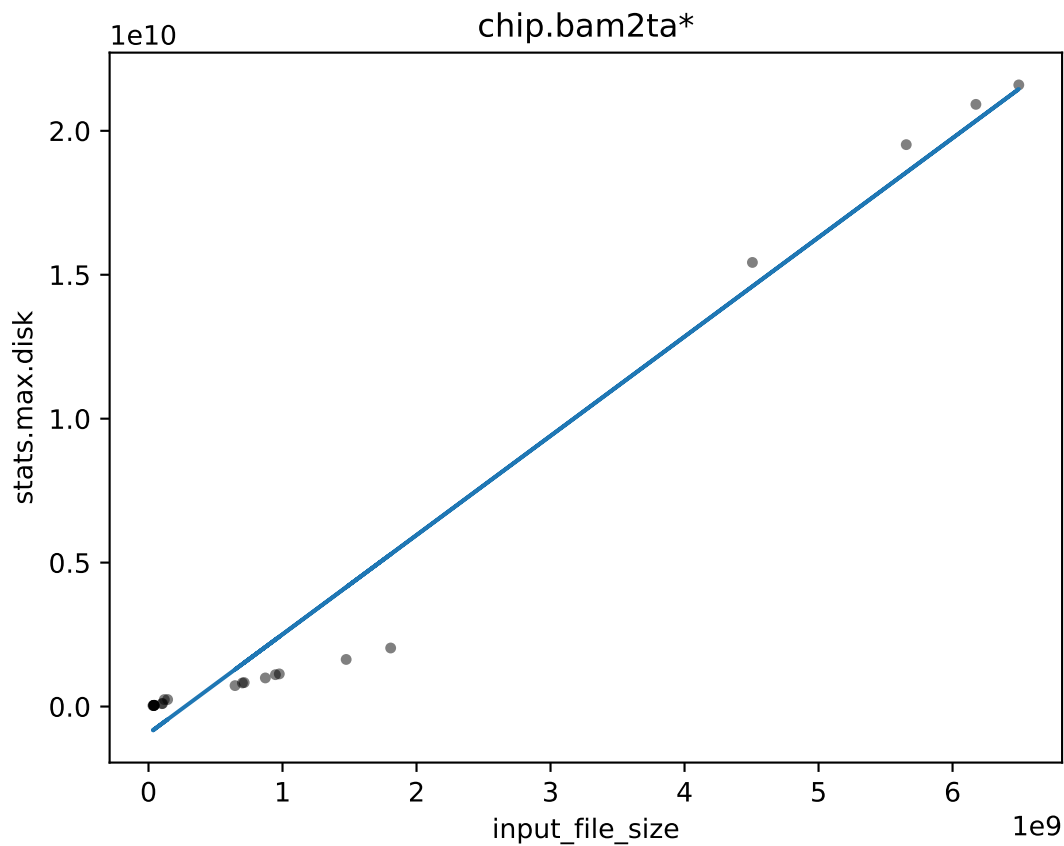


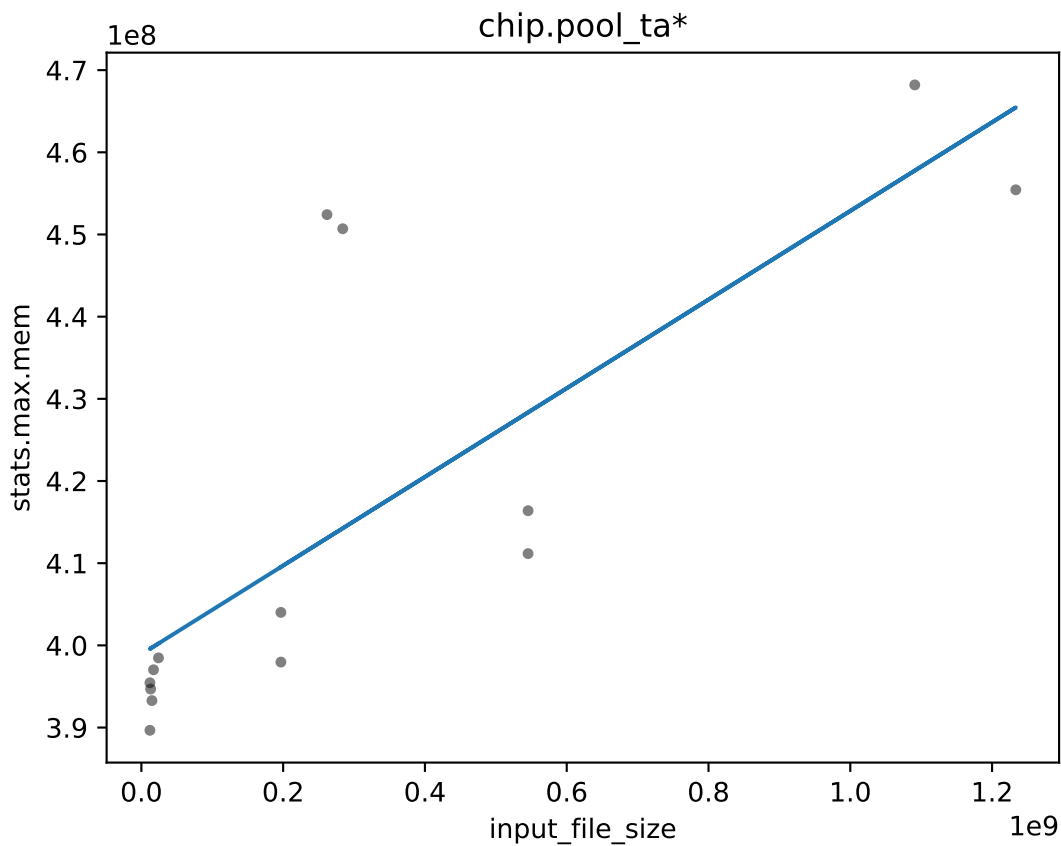


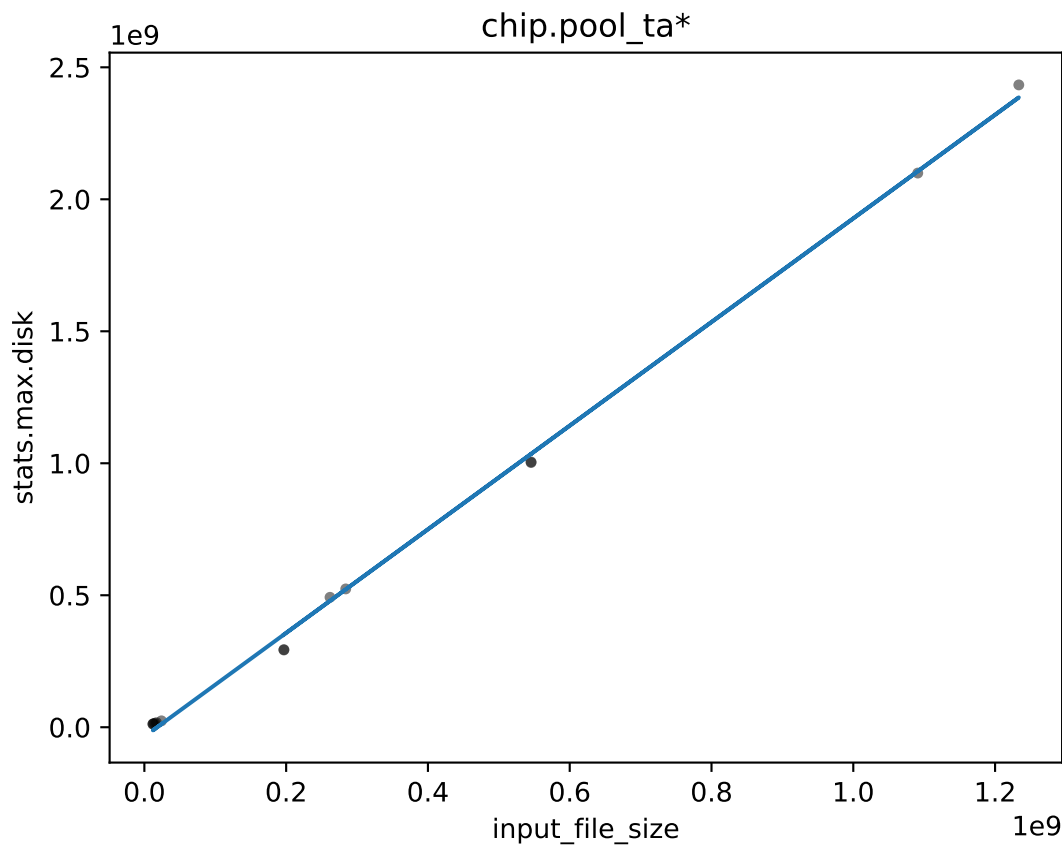












chip.spr*

1e9

stats.max.mem

3.0

2.5

2.0

1.5

1.0

0.5

0

1

2

3

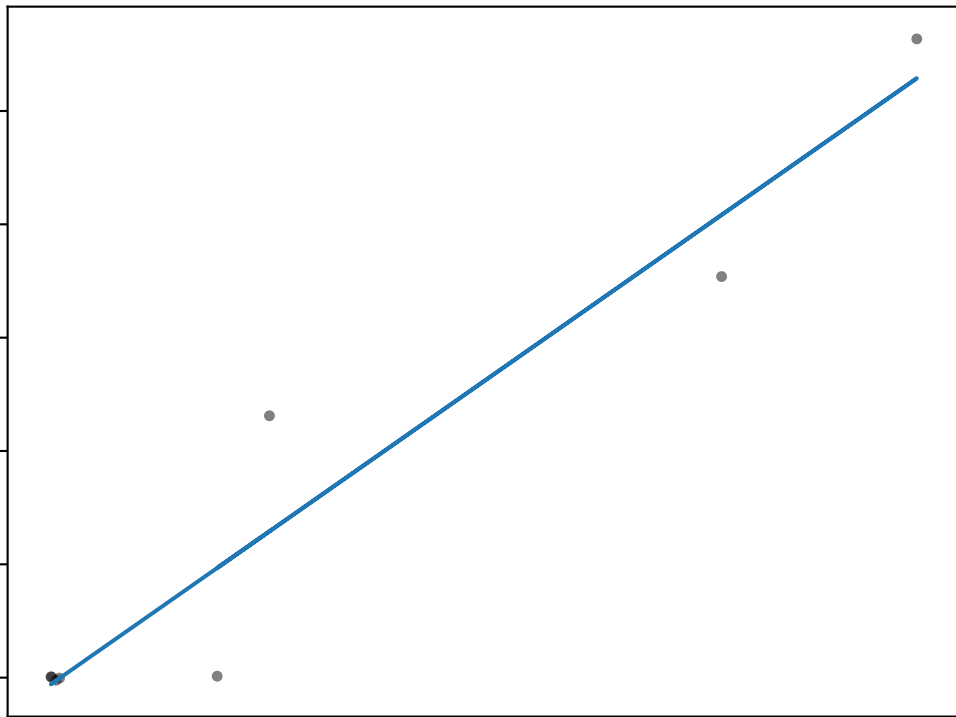
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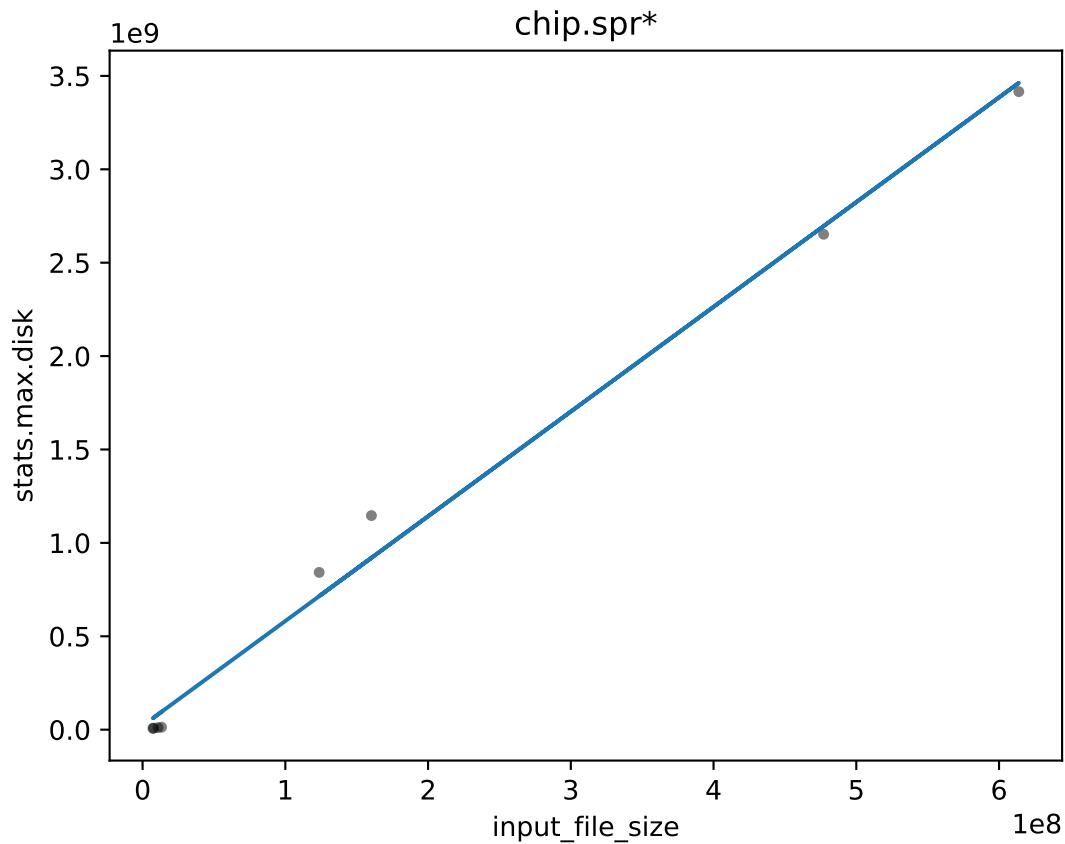
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6

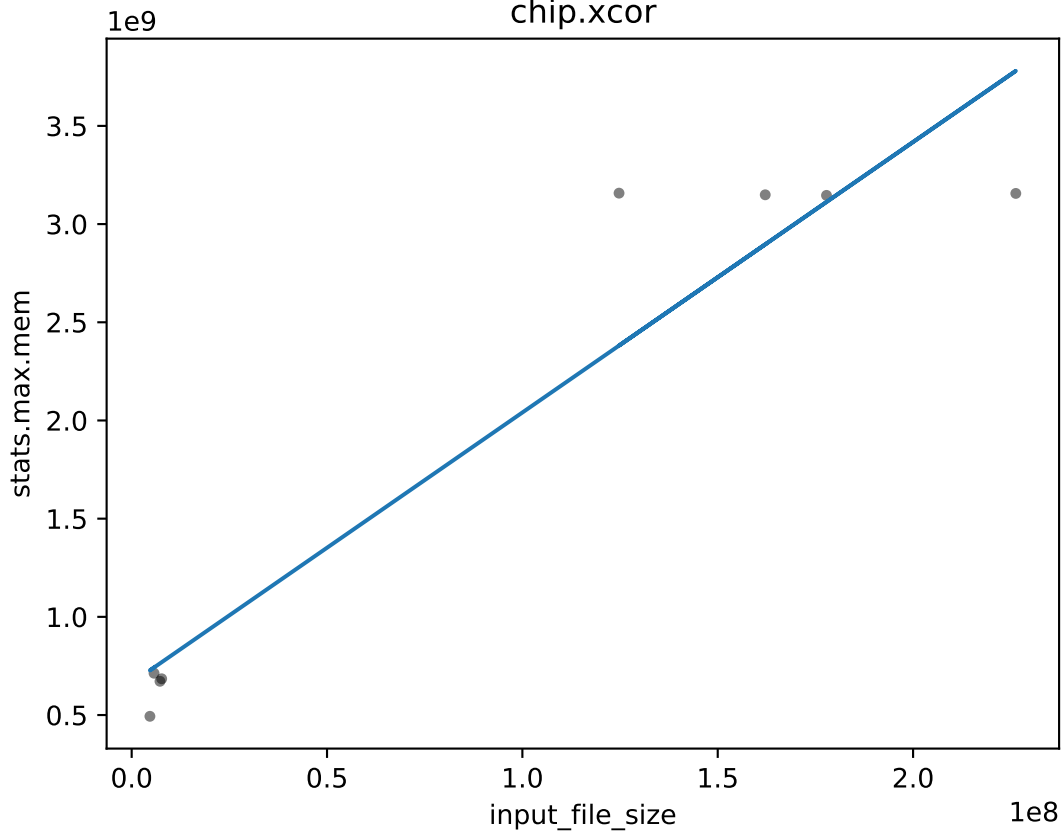
input_file_size

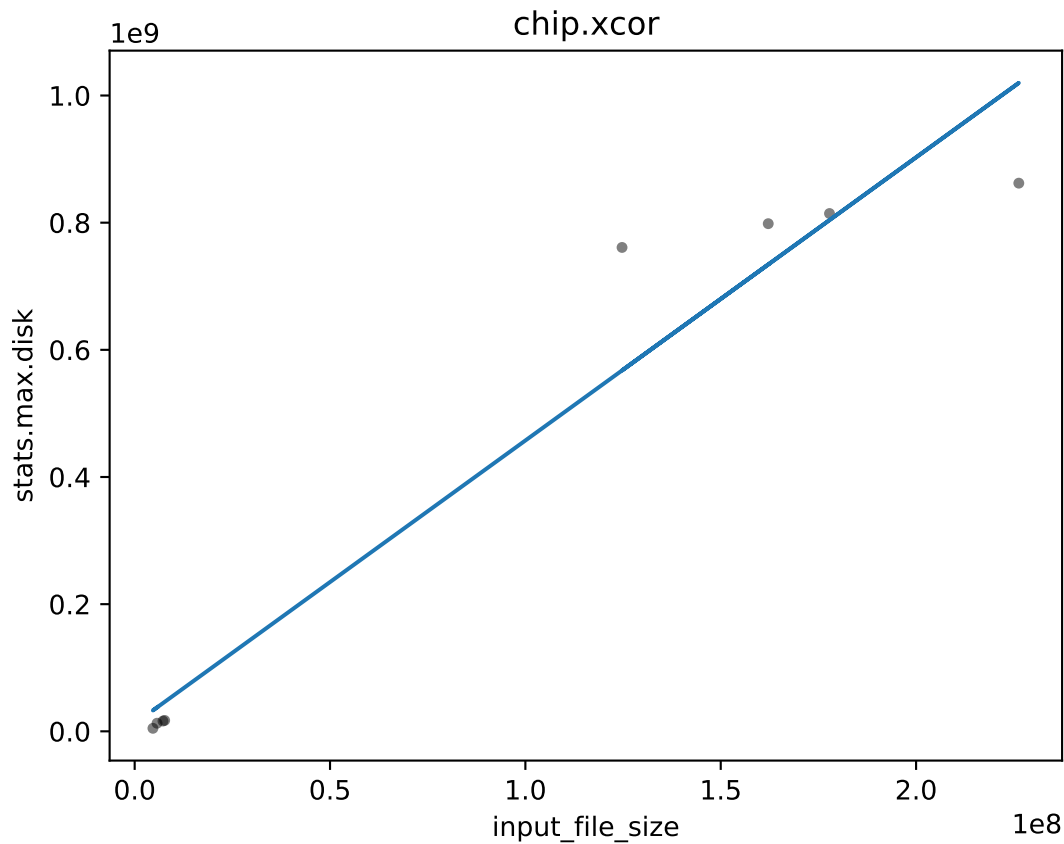
1e8



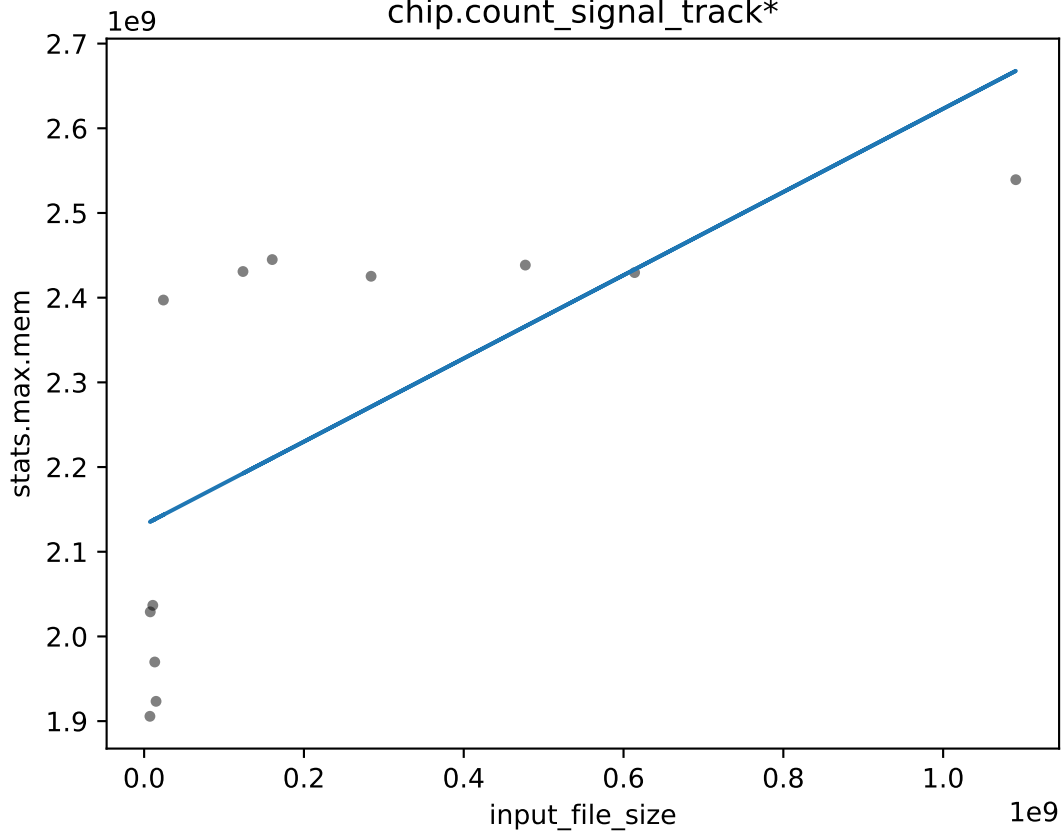


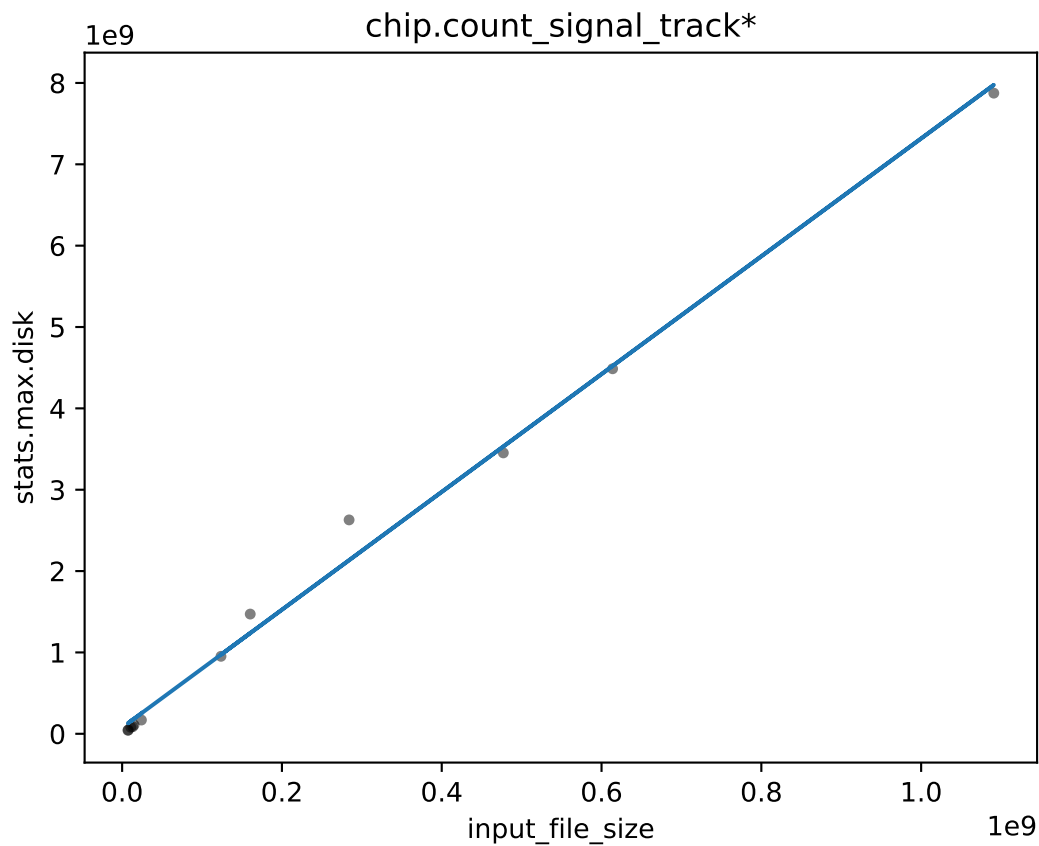
chip.xcor



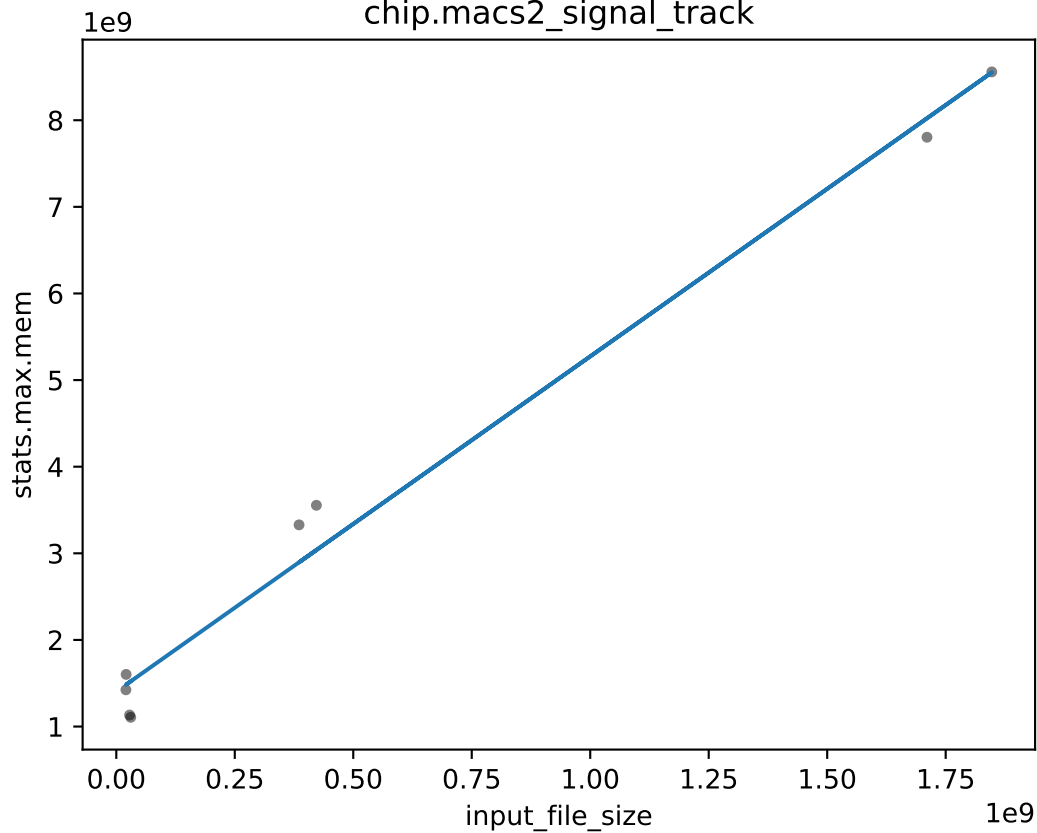


chip.count_signal_track*

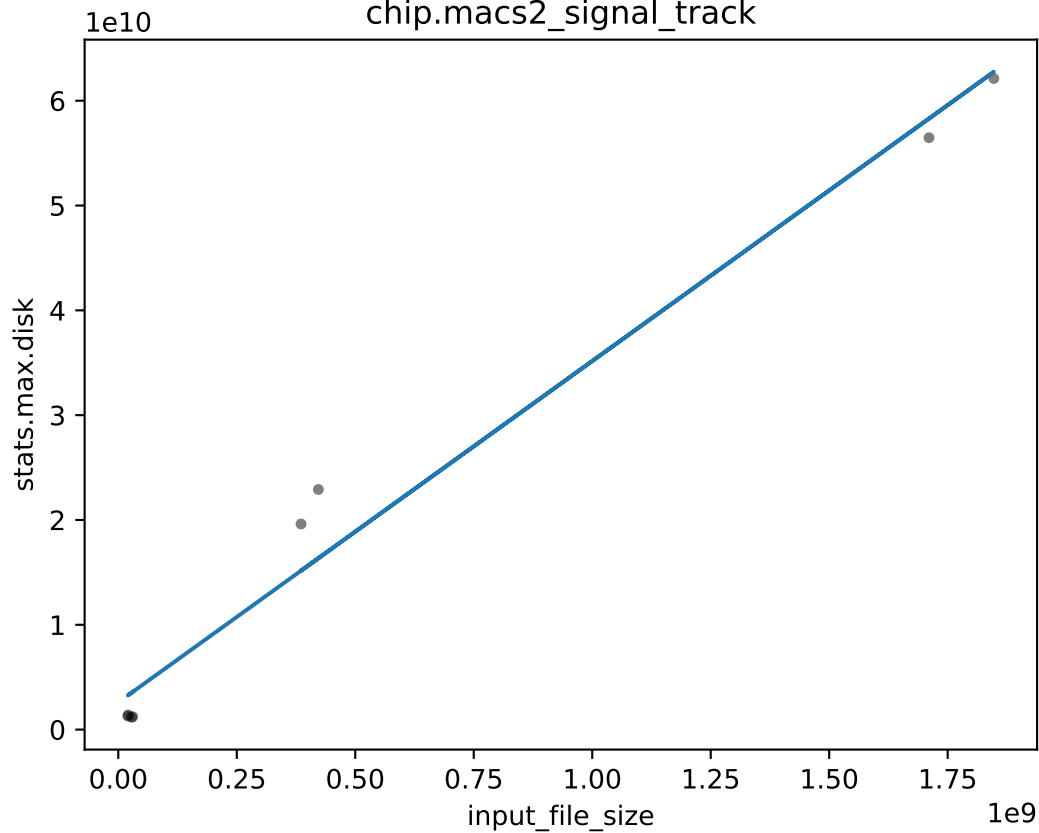




chip.macs2_signal_track



chip.macs2_signal_track



chip.call_peak*

